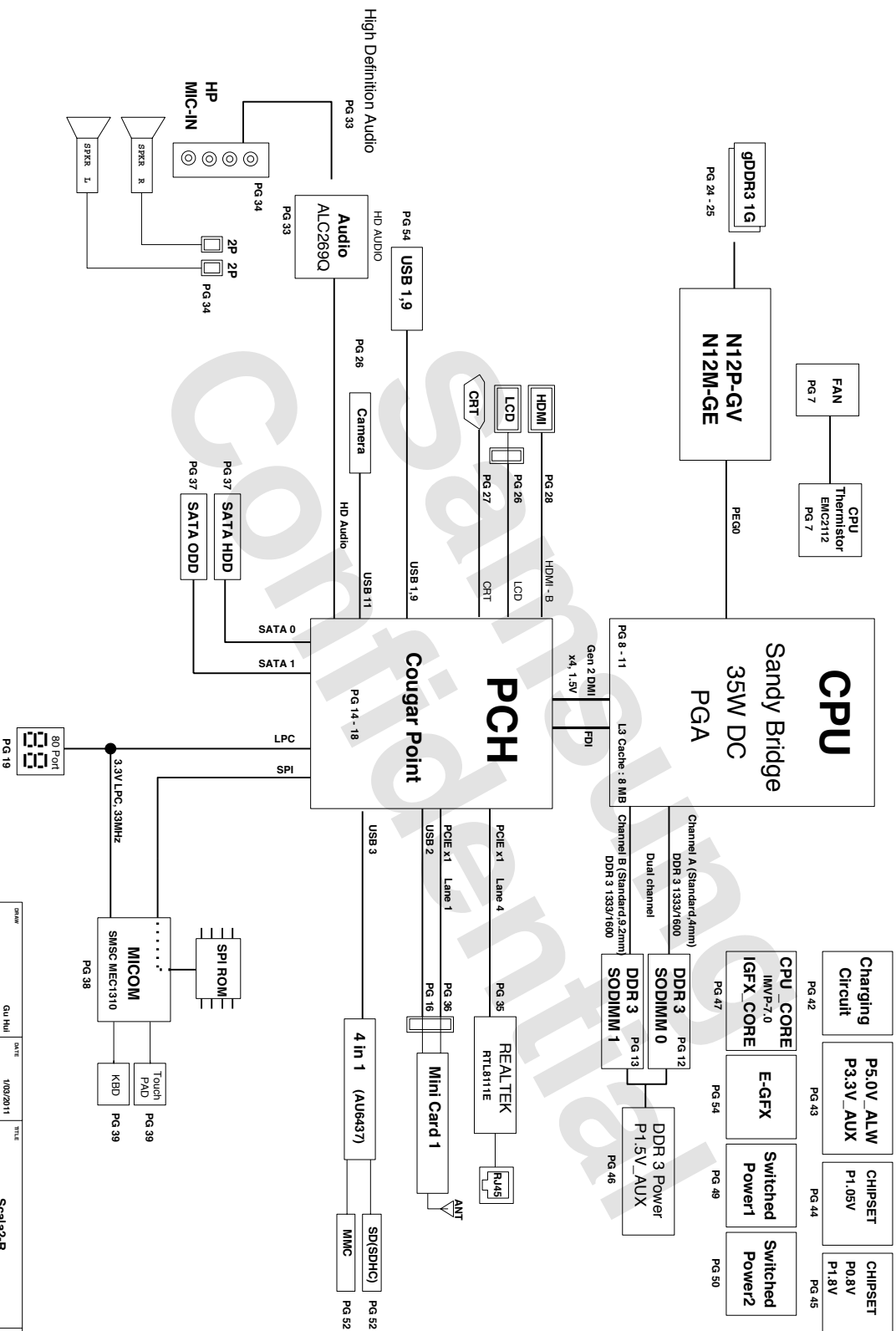


BLOCK DIAGRAM



NAME	GUI HUI	DATE	1/10/2011	TITLE	Scala2-R
CHICK	SA WU	REV STEP	ADV	MAIN	MAIN
APPROVAL	BC LEE	REV	REV 0.1	BLOCK DIAGRAM	BLOCK DIAGRAM
MODULE CODE	undefined	LAST EDIT	January 20, 2011 18:59:20 PM	PAGE	2 OF 48

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

BOARD INFORMATION

Voltage Rails	Active in
VCC P3.3V_MICOM P5.0V_STB P5.0V_ALW P3.3V_ALW P12.0V_ALW	S4 - S5
P5.0V_AUX P3.3V_AUX P1.5V_AUX	
P5.0V P3.3V P1.8V P1.5V P0.8V	
P0.8V	
VCC_CORE EGFX_CORE P1.05V(VCCP)	
P3.3V_D P1.05V_D	S0
P1.05V_D	

USB Port Assign	PCI Express Assign
PORT #	PORT #
0 NC	1 Main Card 1 (M.LAN)
1 SYSTEM PORT 0	2 NC
2 Mini PCI Express	3 NC
3 Multi Memory Card Controller	4 LAN CONTROLLER
4 NC	5 USB 3.0
5 NC	6 NC
6 NC (N/A WITH HM65)	7 NC
7 NC (N/A WITH HM65)	8 Camera
8 SYSTEM PORT 1	9 NC
9 NC	10 NC
10 SYSTEM PORT 1	11 Camera(LCD Cable)
11 NC	12 NC
12 Camera(LCD Cable)	13 NC

SATA PORT Assign
PORT #
0 HDD
1 ODD
2 (N/A WITH HM65)
3 (N/A WITH HM65)
4
5

Crystal / Oscillator			
TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	HM65	Real Time Clock
Crystal	25MHz	LAN	Intel LAN
Crystal	24MHz	USB 3.0	
Crystal	25MHz	HM65	INTEL
Crystal	27MHz	N12P-LP	NVIDIA

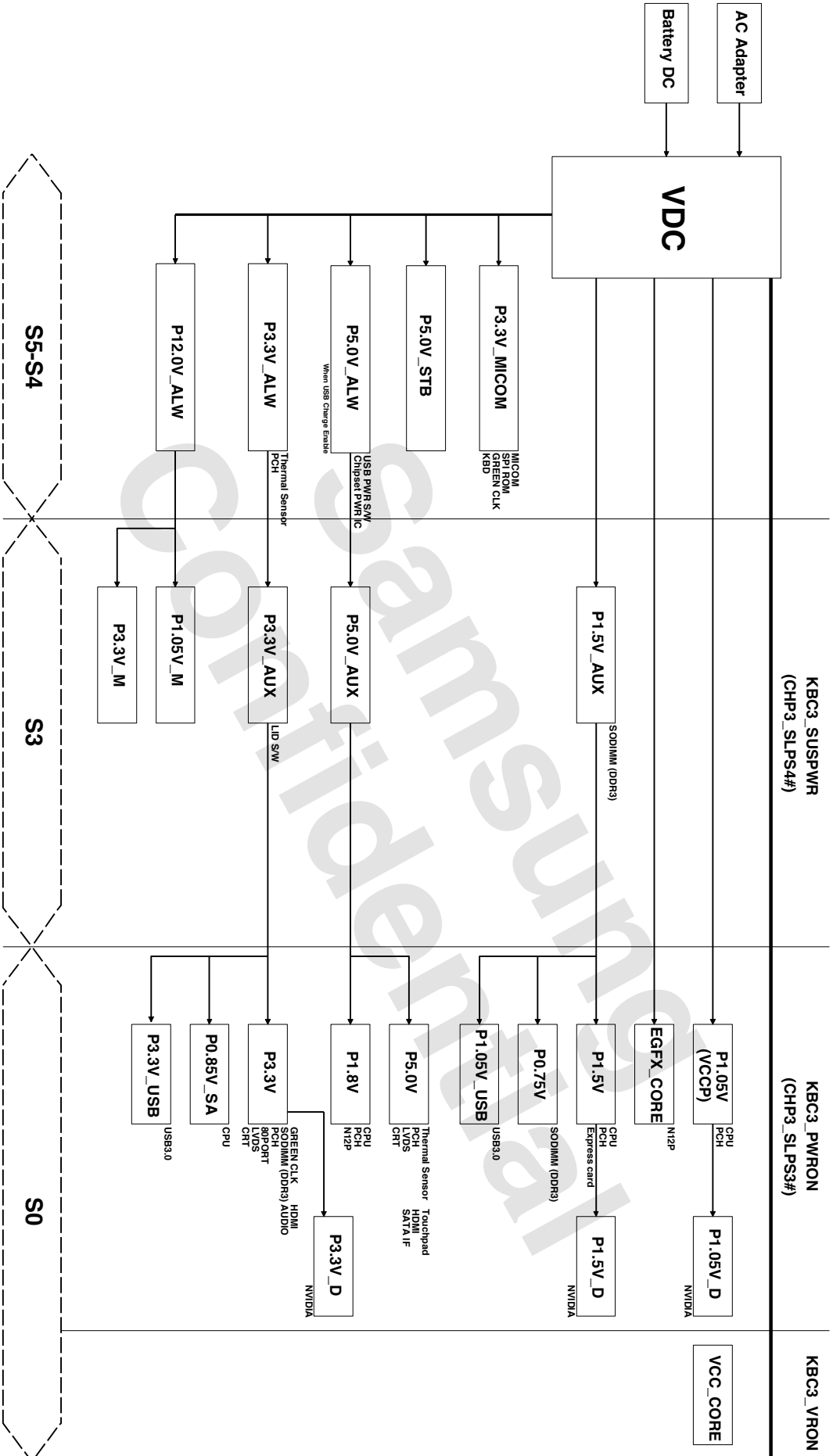
LCD Pannel Detect (TBD)		
Devices	Resolution	PANNEL_DETECT_0

I ² C / SMB Address			
Devices	Address	Hex	Bus
HM65	Master	-	SMBUS Master
GPU Thermal Sensor	0111 101x	7Ah	Thermal Sensor
SODIMM0	1010 100x	A4h	
SODIMM1	1010 010x	30h	
Thermal Sensor on SODIMM0	0011 000x	34h	
Thermal Sensor on SODIMM1	0011 010x	98h	
Thermal Sensor on board	1101 100x	96h	
Power Thermal management TS	1101 011x		

DATE	1/02/2011	FILE	Scal2-R	SAMSUNG
CHKD	SJ WU	REV STEP	MAIN	ELECTRONICS
APPROVAL	BC LEE	REV	ADV	
MODULE CODE	undefined	LAST EDIT	REV. 0.1	BA41-01XXXX
January 20, 2011 16:59:20 PM			PAGE	3 OF 48

POWER DIAGRAM

Rev 0.8

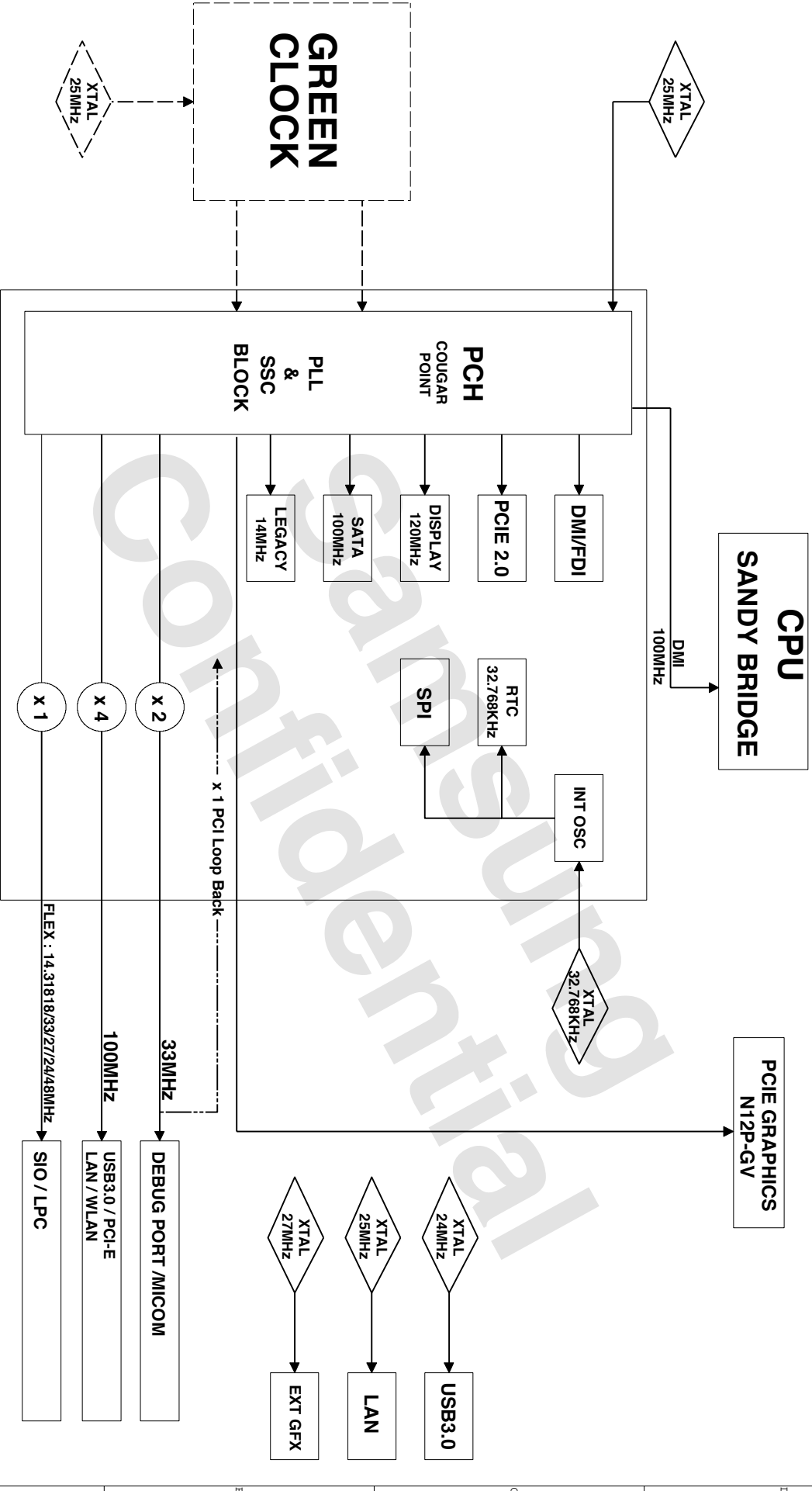


S5-S4

S3

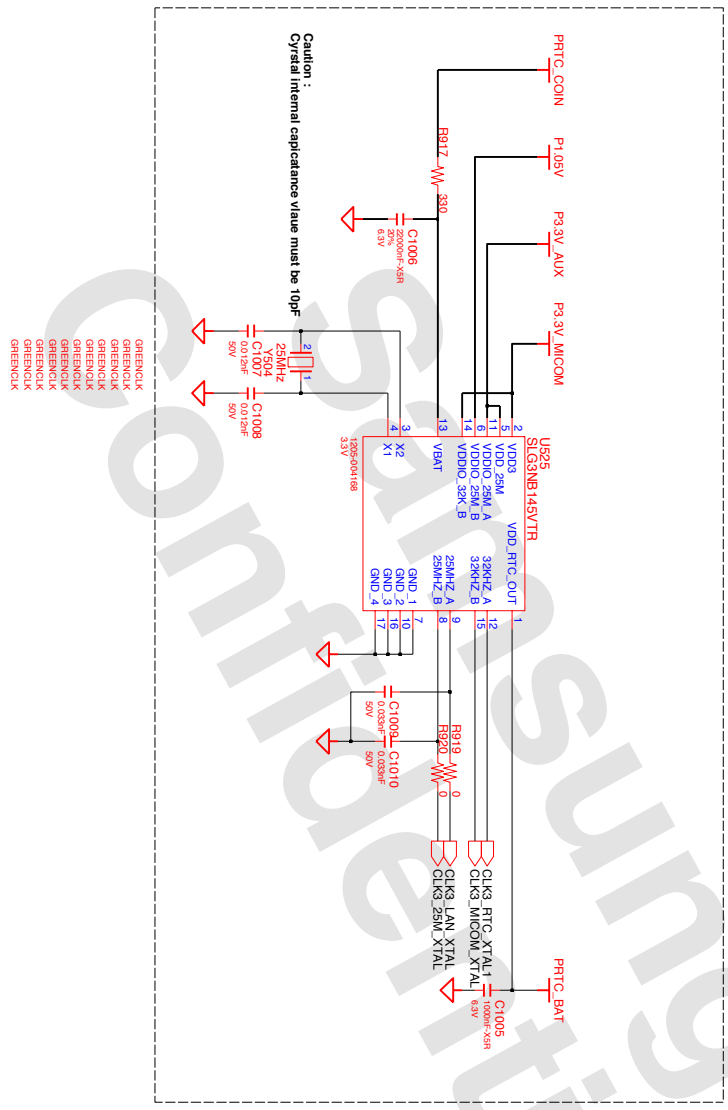
S0

DATE	1/10/2011	TITLE	Scalaz-R MAIN POWER DIAGRAM	SAMSUNG ELECTRONICS
CHK	SJ WU	ADV		BA41-01XXXX
APPROVAL	BC LEE	REV. 0.1		
MODULE CODE	undefined	LAST EDIT	January 20, 2011 18:59:20 PM	PAGE 4 OF 48



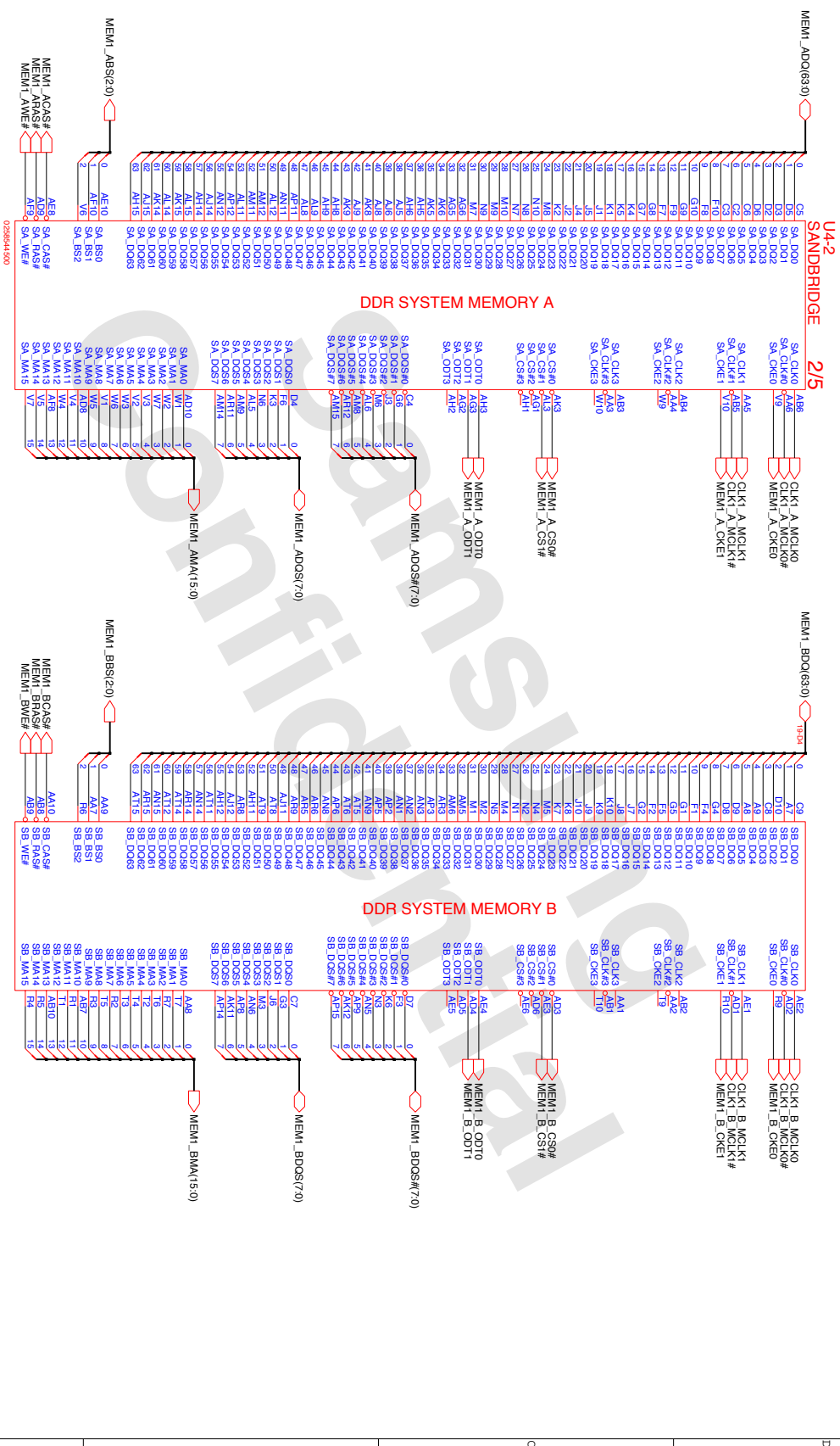
DATE	10/20/11	FILE	Scal2-R	SAMSUNG
CHICK	SJ WU	REV STEP	MAIN	ELECTRONICS
APPROVAL	BC LEE	REV	CLOCK DIAGRAM	BA41-01XXXX
MODULE CODE	undefined	LAST EDIT	January 20, 2011 18:59:20 PM	PAGE 5 OF 48

CLOCK DISTRIBUTOR



ENGINEER	DATE	TITLE	SAMSUNG
DESIGNED BY	REV. STEP	SCAL A2-R_PV_MAIN	ELECTRONICS
CHANGED BY	REV	Module Name	BA41-XXXXXX
PG. OK	LAST EDIT	1.0	PAGE 6 OF XX

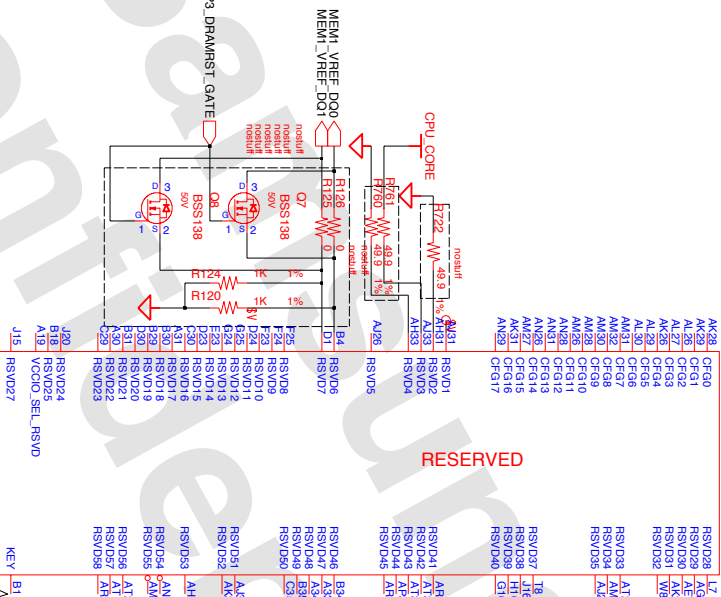




DATE	TITLE		SAMSUNG ELECTRONICS PART NO. BA41-01XXXX	
CHECK	1/03/2011	Scalaz-R CPU		
APPROVAL	DNV 01P ADV			
MODEL CODE	SA WU BC LEE	SANDY BRIDGE (2/4)		
	REV. 0.1			
UNFILED	LAST EXT	PAGE 8 OF 48	January 20, 2011 10:55:20 PM	

U4-5 SANDBRIDGE 5/5

VS1	VS381	VS81	VS161	F2
VS2	VS382	VS82	VS162	F3
VS3	VS383	VS83	VS163	F4
VS4	VS384	VS84	VS164	F5
VS5	VS385	VS85	VS165	F6
VS6	VS386	VS86	VS166	F7
VS7	VS387	VS87	VS167	F8
VS8	VS388	VS88	VS168	F9
VS9	VS389	VS89	VS169	F10
VS10	VS390	VS90	VS170	F11
VS11	VS391	VS91	VS171	F12
VS12	VS392	VS92	VS172	F13
VS13	VS393	VS93	VS173	F14
VS14	VS394	VS94	VS174	F15
VS15	VS395	VS95	VS175	F16
VS16	VS396	VS96	VS176	F17
VS17	VS397	VS97	VS177	F18
VS18	VS398	VS98	VS178	F19
VS19	VS399	VS99	VS179	F20
VS20	VS400	VS100	VS180	F21
VS21	VS401	VS101	VS181	F22
VS22	VS402	VS102	VS182	F23
VS23	VS403	VS103	VS183	F24
VS24	VS404	VS104	VS184	F25
VS25	VS405	VS105	VS185	F26
VS26	VS406	VS106	VS186	F27
VS27	VS407	VS107	VS187	F28
VS28	VS408	VS108	VS188	F29
VS29	VS409	VS109	VS189	F30
VS30	VS410	VS110	VS190	F31
VS31	VS411	VS111	VS191	F32
VS32	VS412	VS112	VS192	F33
VS33	VS413	VS113	VS193	F34
VS34	VS414	VS114	VS194	F35
VS35	VS415	VS115	VS195	F36
VS36	VS416	VS116	VS196	F37
VS37	VS417	VS117	VS197	F38
VS38	VS418	VS118	VS198	F39
VS39	VS419	VS119	VS199	F40
VS40	VS420	VS120	VS200	F41
VS41	VS421	VS121	VS201	F42
VS42	VS422	VS122	VS202	F43
VS43	VS423	VS123	VS203	F44
VS44	VS424	VS124	VS204	F45
VS45	VS425	VS125	VS205	F46
VS46	VS426	VS126	VS206	F47
VS47	VS427	VS127	VS207	F48
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VS49	VS429	VS129	VS209	F50
VS50	VS430	VS130	VS210	F51
VS51	VS431	VS131	VS211	F52
VS52	VS432	VS132	VS212	F53
VS53	VS433	VS133	VS213	F54
VS54	VS434	VS134	VS214	F55
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VS57	VS437	VS137	VS217	F58
VS58	VS438	VS138	VS218	F59
VS59	VS439	VS139	VS219	F60
VS60	VS440	VS140	VS220	F61
VS61	VS441	VS141	VS221	F62
VS62	VS442	VS142	VS222	F63
VS63	VS443	VS143	VS223	F64
VS64	VS444	VS144	VS224	F65
VS65	VS445	VS145	VS225	F66
VS66	VS446	VS146	VS226	F67
VS67	VS447	VS147	VS227	F68
VS68	VS448	VS148	VS228	F69
VS69	VS449	VS149	VS229	F70
VS70	VS450	VS150	VS230	F71
VS71	VS451	VS151	VS231	F72
VS72	VS452	VS152	VS232	F73
VS73	VS453	VS153	VS233	F74
VS74	VS454	VS154	VS234	F75
VS75	VS455	VS155	VS235	F76
VS76	VS456	VS156	VS236	F77
VS77	VS457	VS157	VS237	F78
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VS79	VS459	VS159	VS239	F80
VS80	VS460	VS160	VS240	F81

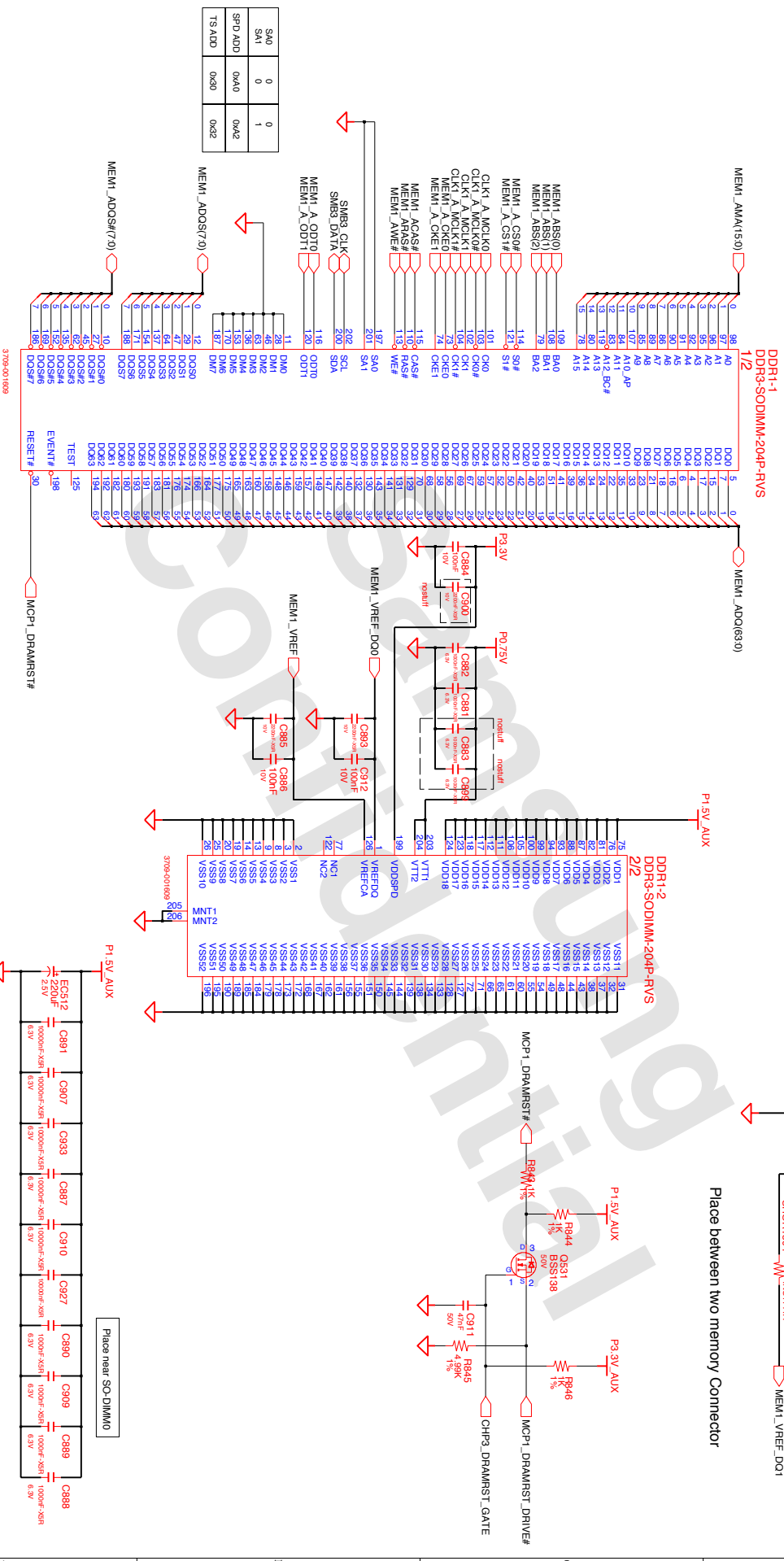


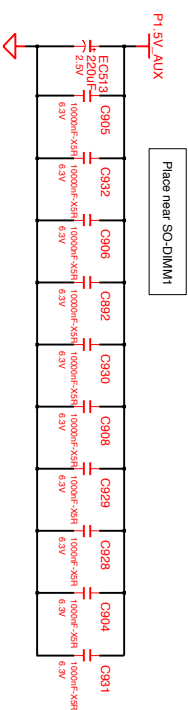
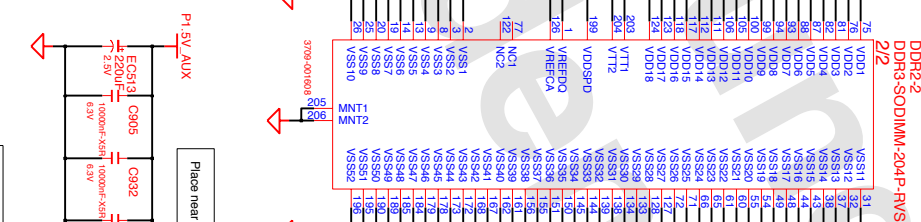
DDR SO-DIMM #0

Height : 9.2 mm (REVERSE)

P/N : 3709-001609 (CONCRAFT)

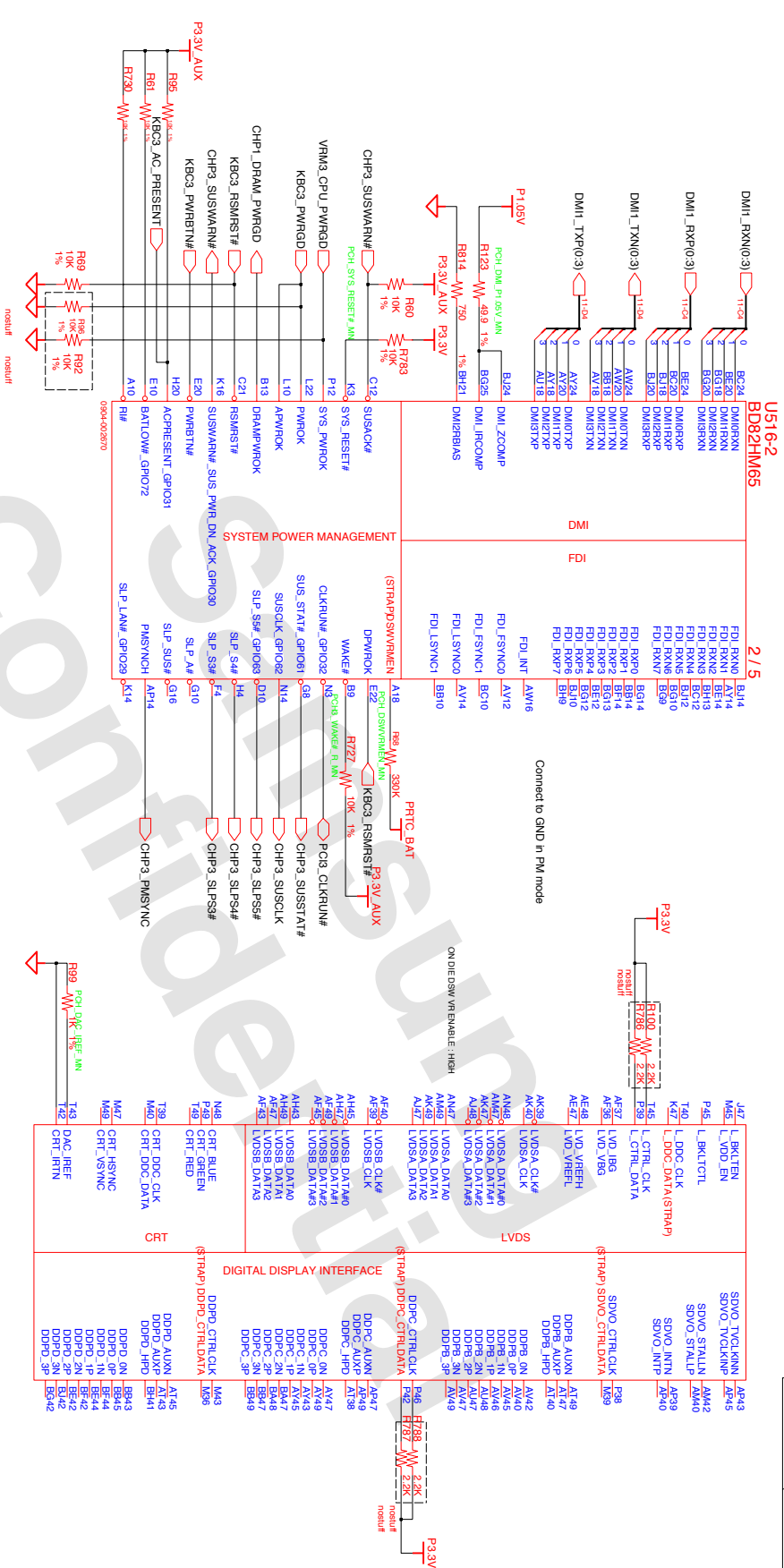
P/N : 3709-001656 (Foxconn)



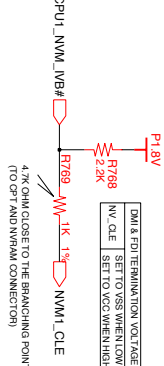
P1.5V_AUX
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NAME	GU HUI	DATE	16/09/2011	TITLE	<div>SAMSUNG</div> <div>ELECTRONICS</div>
CHICK	SI WU	DESIGNER	ADV		
APPROVAL	REV	REV. 0.1			
MOQ/CODE	BC LEE				
		1.0/01/11			
			January 20, 2011 16:59:20 PM		
				PAGE	12 OF 48
					BA41-01XXXXA





NAME	GU HUI	DATE	1/23/2011	SAMSUNG ELECTRONICS
CHECK	SI WU	REV. DES	ADV	
#PROJECT	BC LEE	REV	REV. 0.1	
MODEL CODE		LAST EXT		
January 20, 2011 18:59:20 PM				PAGE 14 OF 48 BA41-01XXXX



U516-3
BD82HM65

3 / 5

PLT3_RST_ORG#

PLT3_RST#

P3 3V AUX

P3 3V

P3 3V AUX

P3 3V

P3 3V AUX

P3 3V

P3 3V AUX

P3 3V

P3 3V AUX

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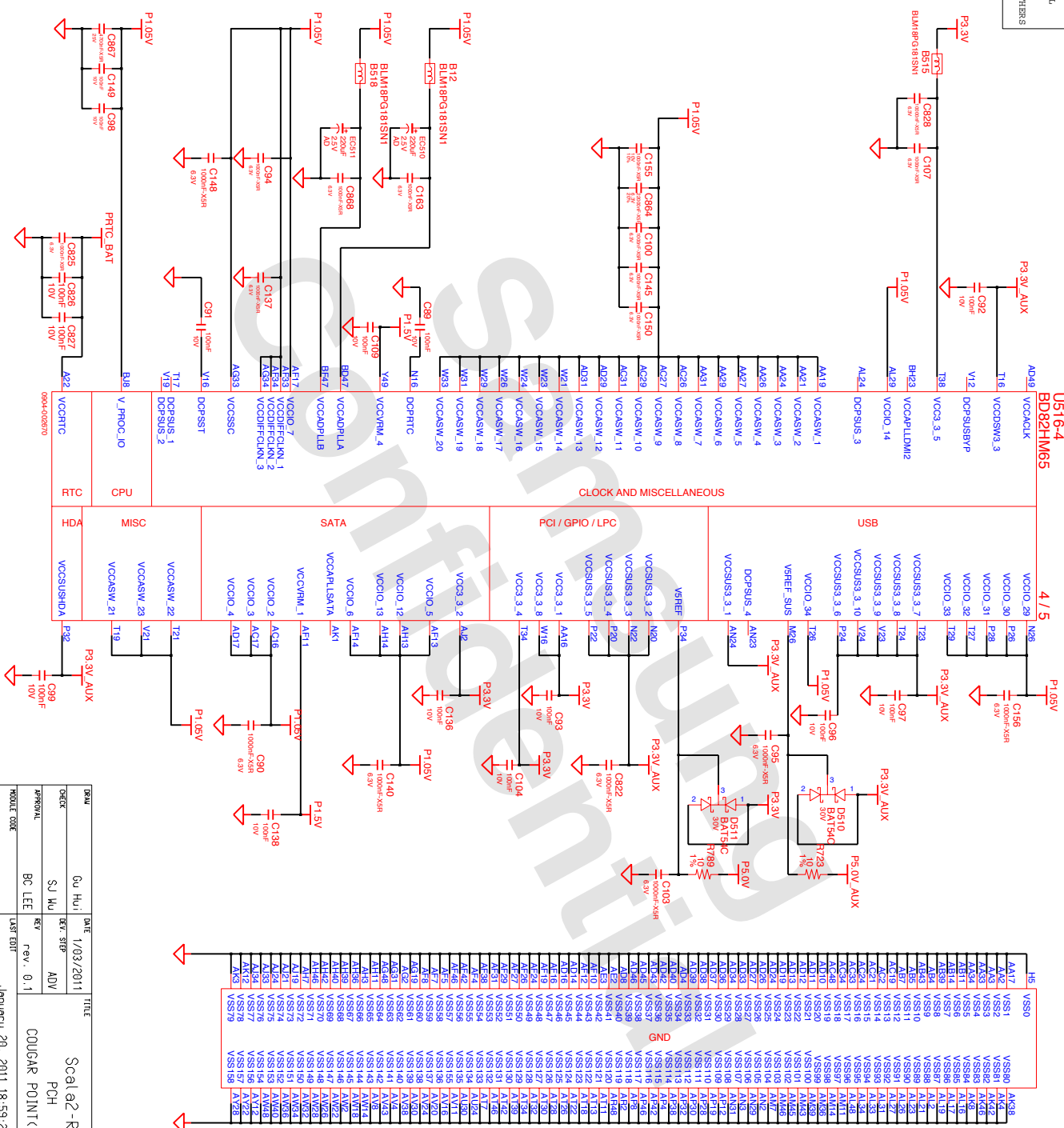
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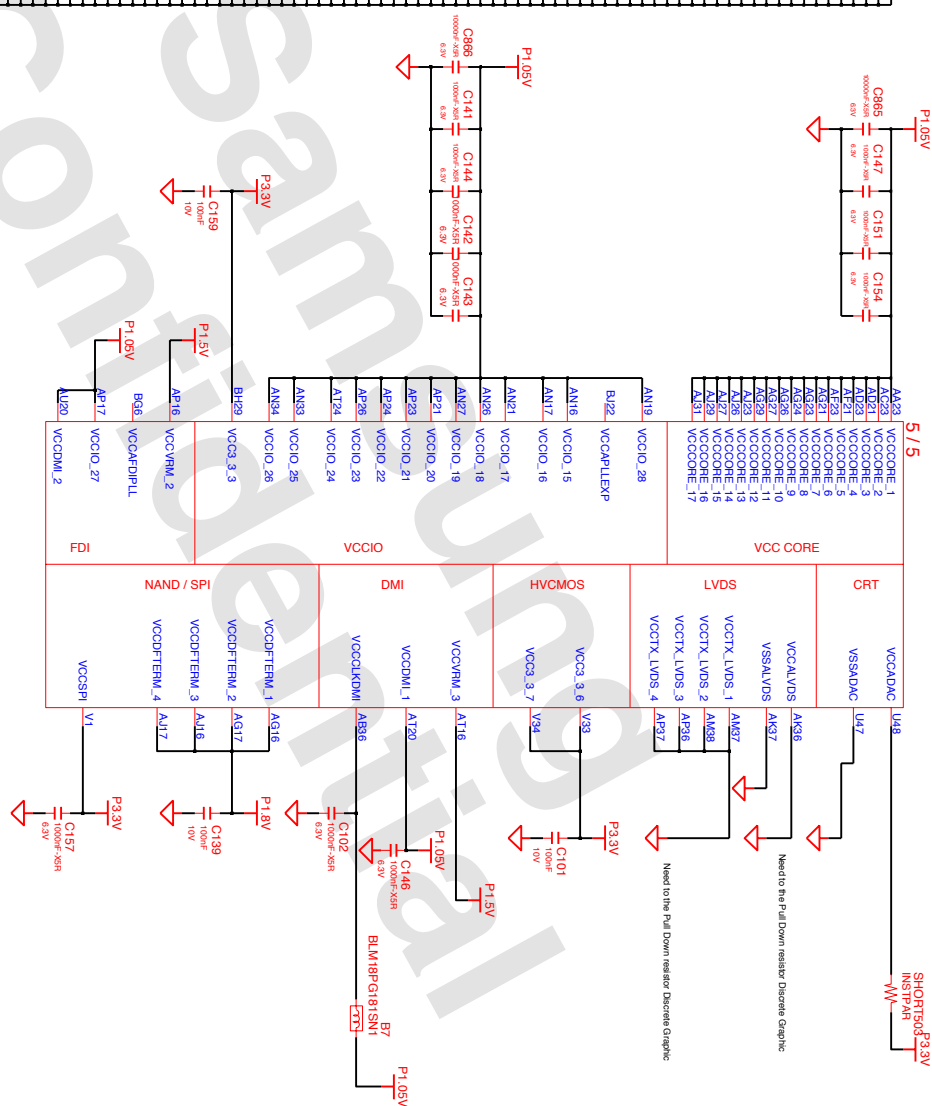
U516-5
 BD821M65

This power share with thermal sensor

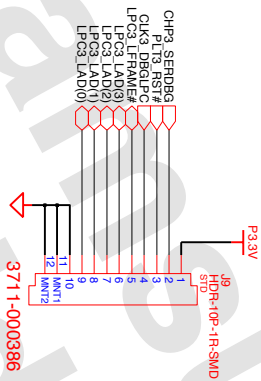
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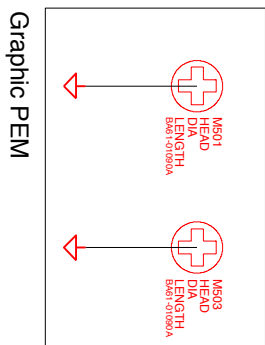
00d4-00d670



80H PORT DEBUG



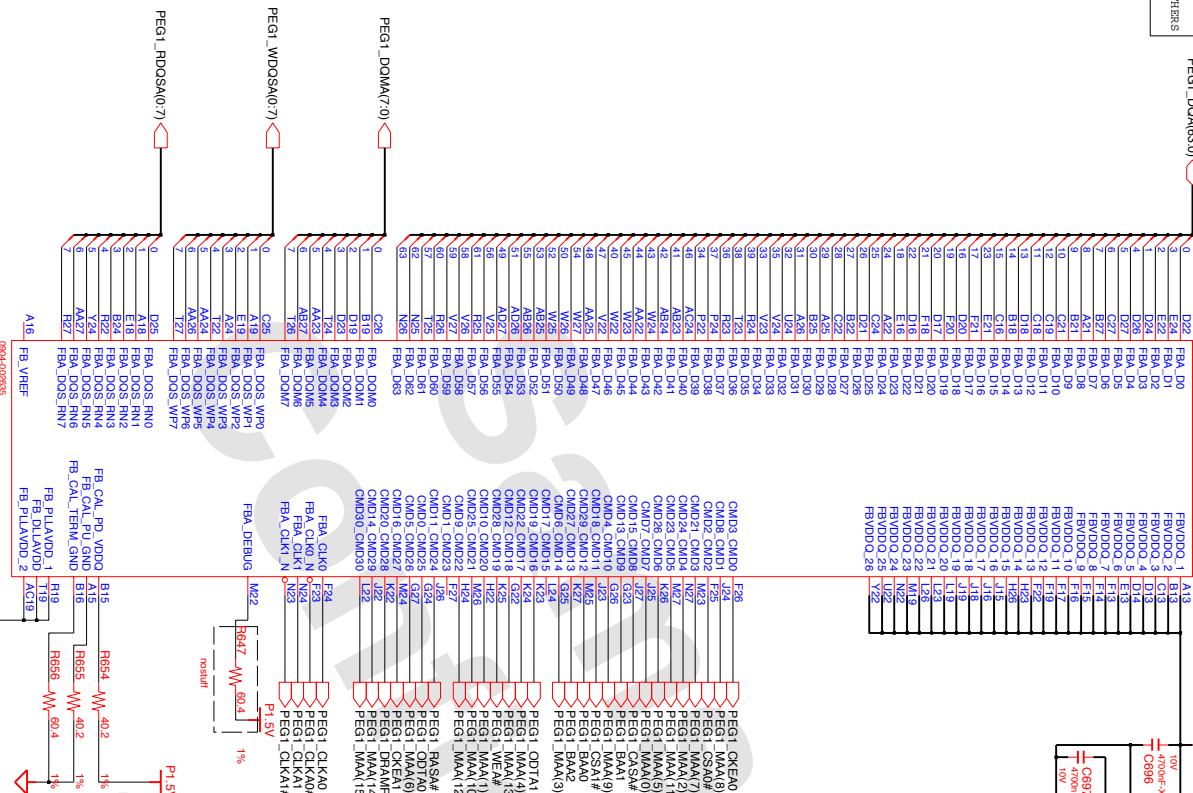
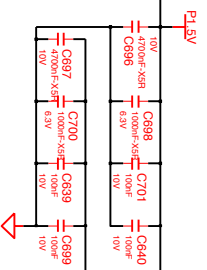
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DESIGN	Gu Hu.	1/03/2011	REV. STP	Scal a2-R	DEBUG PORT	BA41-01XXXX	ELECTRONICS
DESIGN	SJ Wu	ADV	REV. 0.1	80H DECODER			
DESIGN	BC LEE	REV. 0.1	January 20, 2011 18:59:20 PM	PAGE 18	OF 48		



SECTION	QUANTITY	DATE	TITLE	SAMSUNG ELECTRONICS
CHECK	PCV 311P	1/03/2011	Scale2-R	
APPROVAL	SJ.WJ	ADV	N12P_40NM(1/4)	
APPROVAL	REV	REV. 0.1	N12P_40NM(1/4)	
MOUSE CODE	JAN 18 2011	PAGE	19	

U3-2 N12P-GV-SA1

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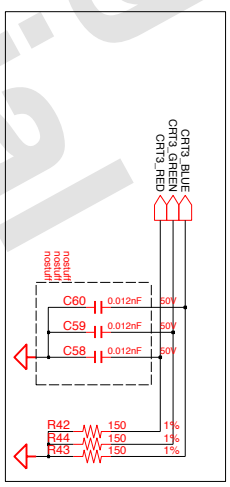


REVISION	DATE	THRU
004-002635	1/10/2011	

DESIGN	CHKD	BY	DATE
004-002635	GAU HUI	REV. 0.1	1/10/2011

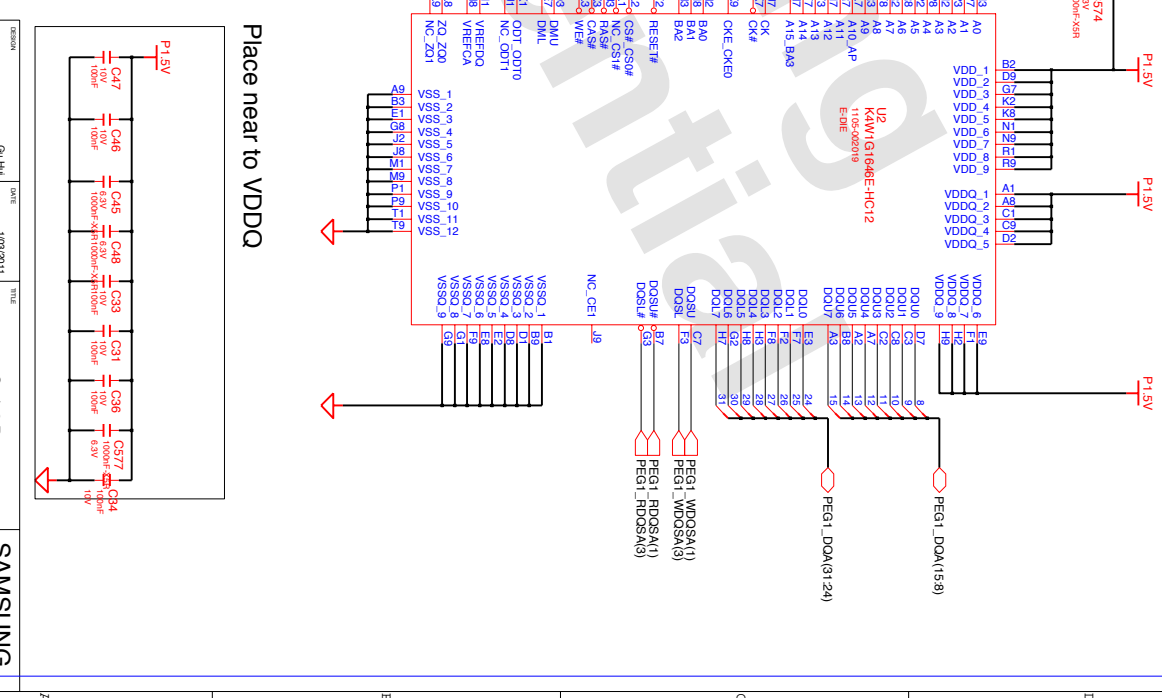
DESIGN	CHKD	BY	DATE
004-002635	GAU HUI	REV. 0.1	1/10/2011

N12M-GE : CMD MODE C
N12P-GV : CMD MODE E

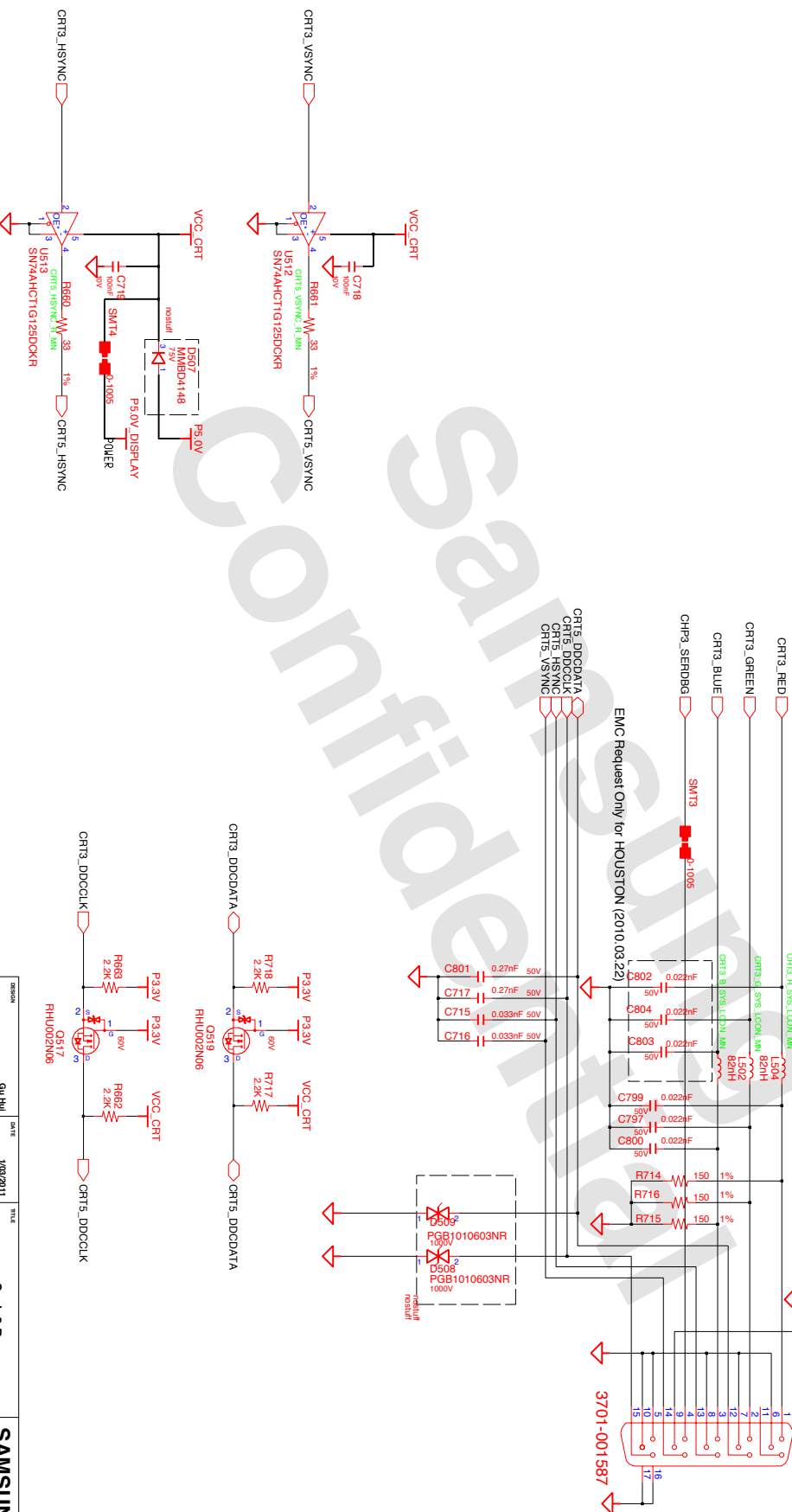


U3-5





CRT LOGIC

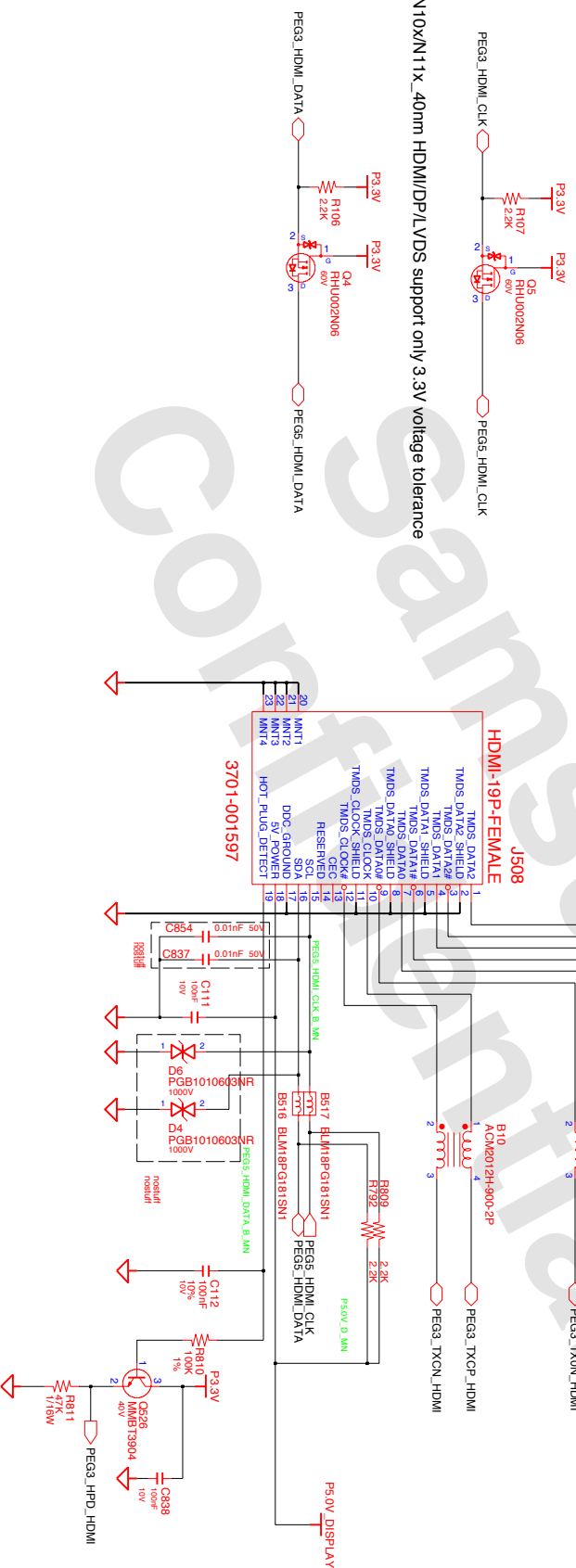


DESIGNER	GU HUI	DATE	1/10/2011	FILE	Scala2-R
CHECK	SJ WU	REV. STEP	ADV		GRAPHICS_I/F
APPROVAL	BC LEE	REV.	REV. 0.1		CRT
MODULE CODE		LAT EDIT			
			January 20, 2011 10:59:20 PM	PAGE	26 OF 48
					SAMSUNG ELECTRONICS
					BA41-01XXXXA

HDMI Graphic

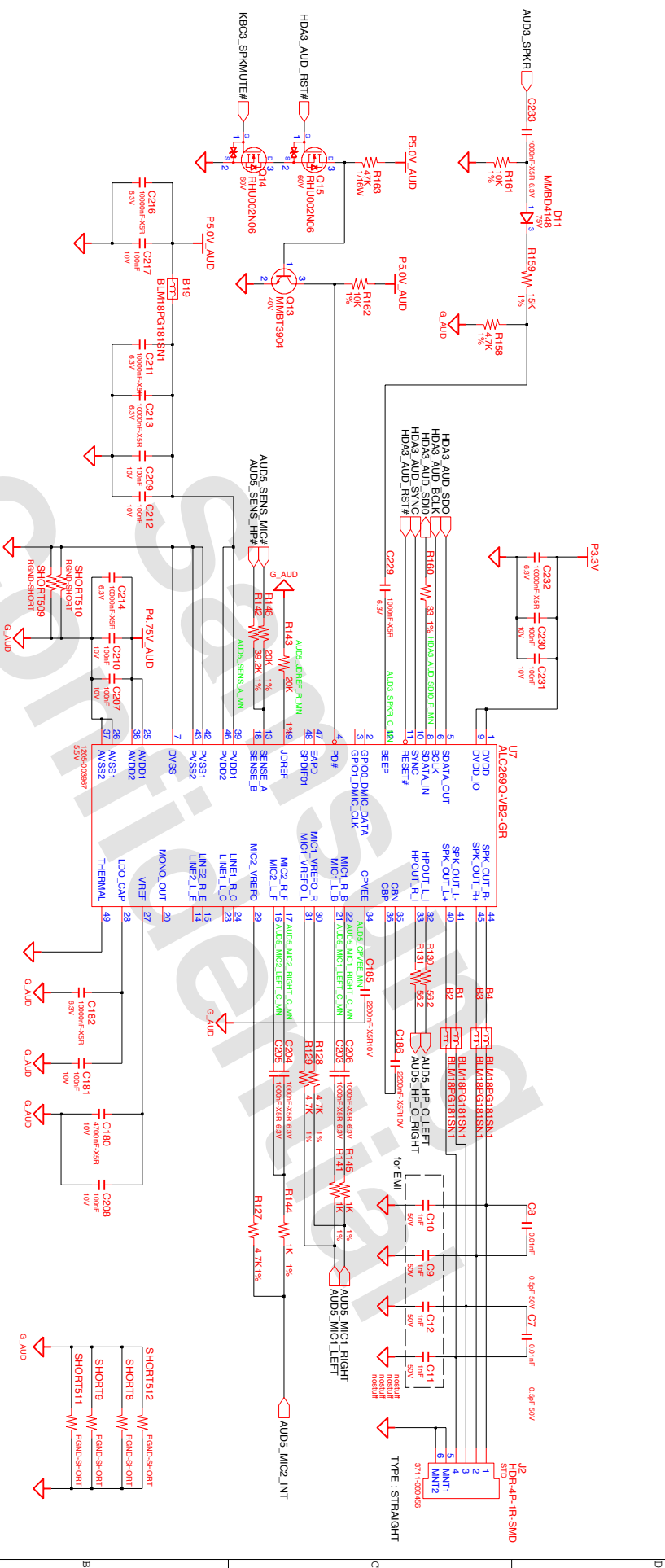
FOR HDMI VERIFICATION TEST

N10X/N11x_40nm HDMI/DPLVDS support only 3.3V voltage tolerance

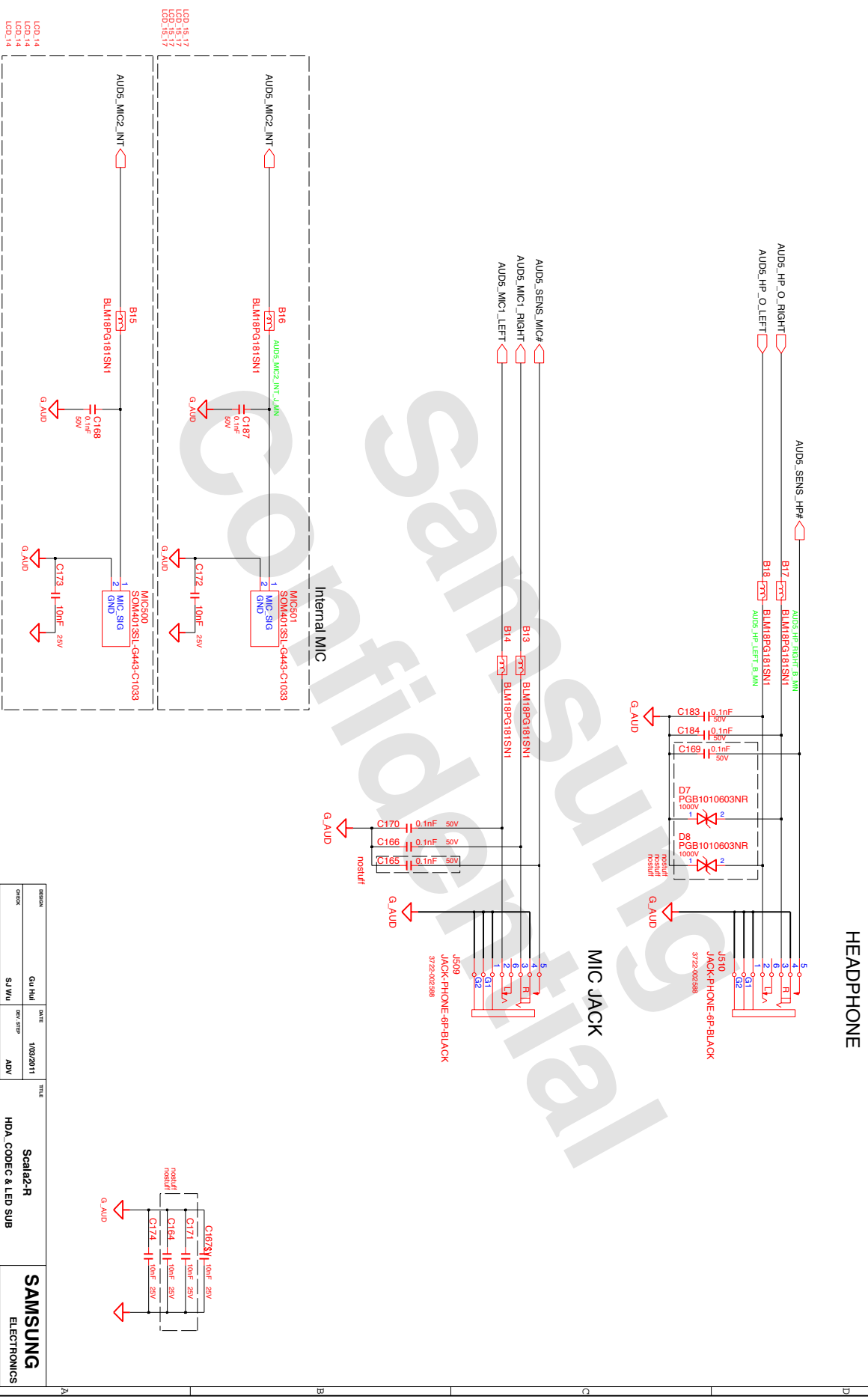


DESIGN	GU HUI	DATE	1/10/2011	TITLE	Scale2-R	SAMSUNG
CHECK	SJ WU	REV. STEP	ADV	GRAPHICS_IF	GRAPHICS_IF	ELECTRONICS
APPROVAL	BC LEE	REV.	REV. 0.1	HDMI	HDMI	BA41-01XXXXA
MODULE CODE		LAST EDIT	January 20, 2011 18:58:20 PM	PAGE	27	OF 48

HD AUDIO

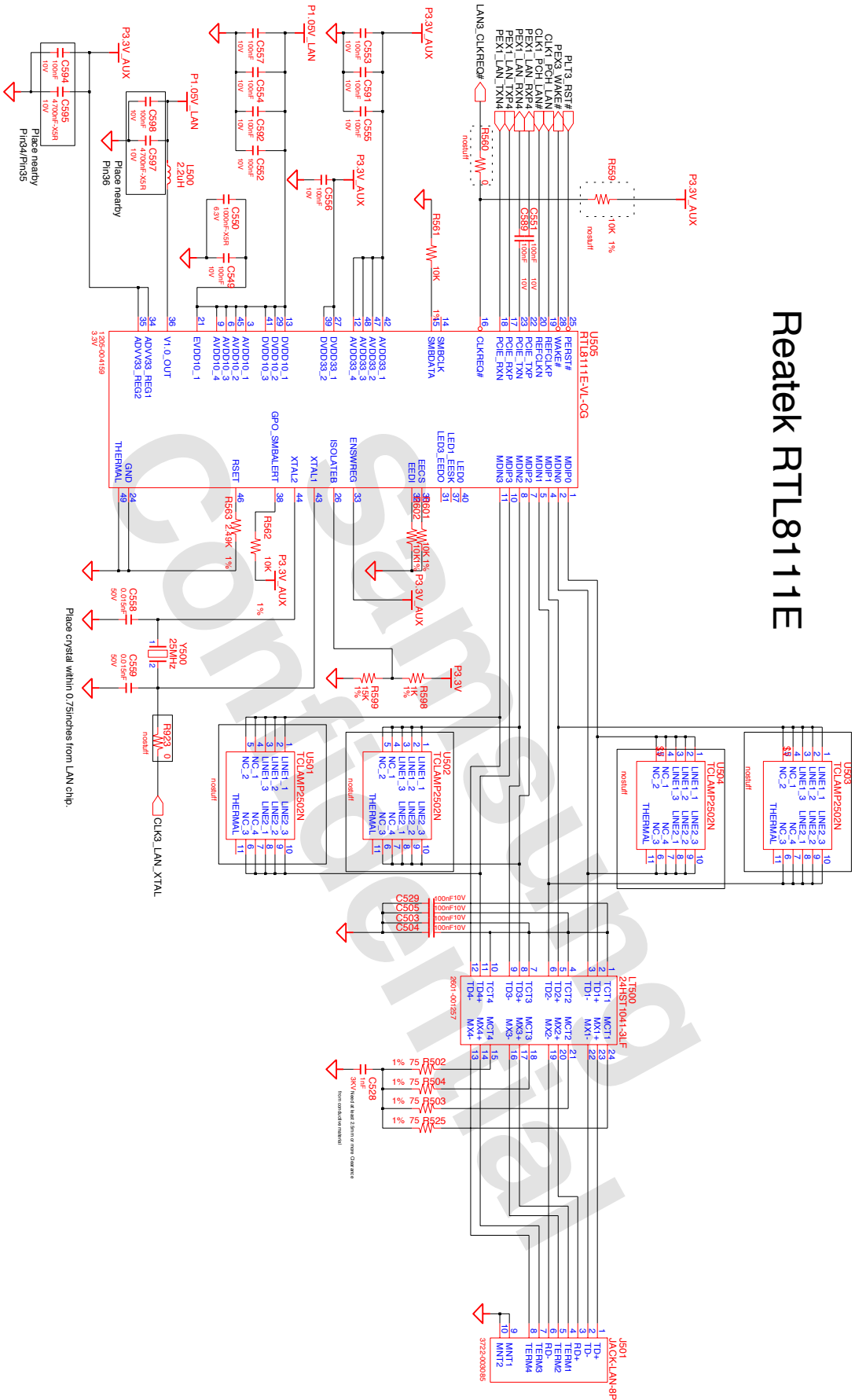


REVISION	DATE	FILE	SAMSUNG ELECTRONICS HDA CODEC SUB AUDIO CODEC ALC2680 SUB January 26, 2011 16:59:20 PM BAA1-01XXXX
CHICK	REV. STEP	ADV	
APPROVAL	REV.	REV. 0.1	
MODULE CODE	LAST EDIT	PAGE 28 OF 48	

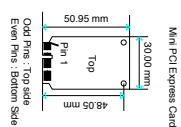
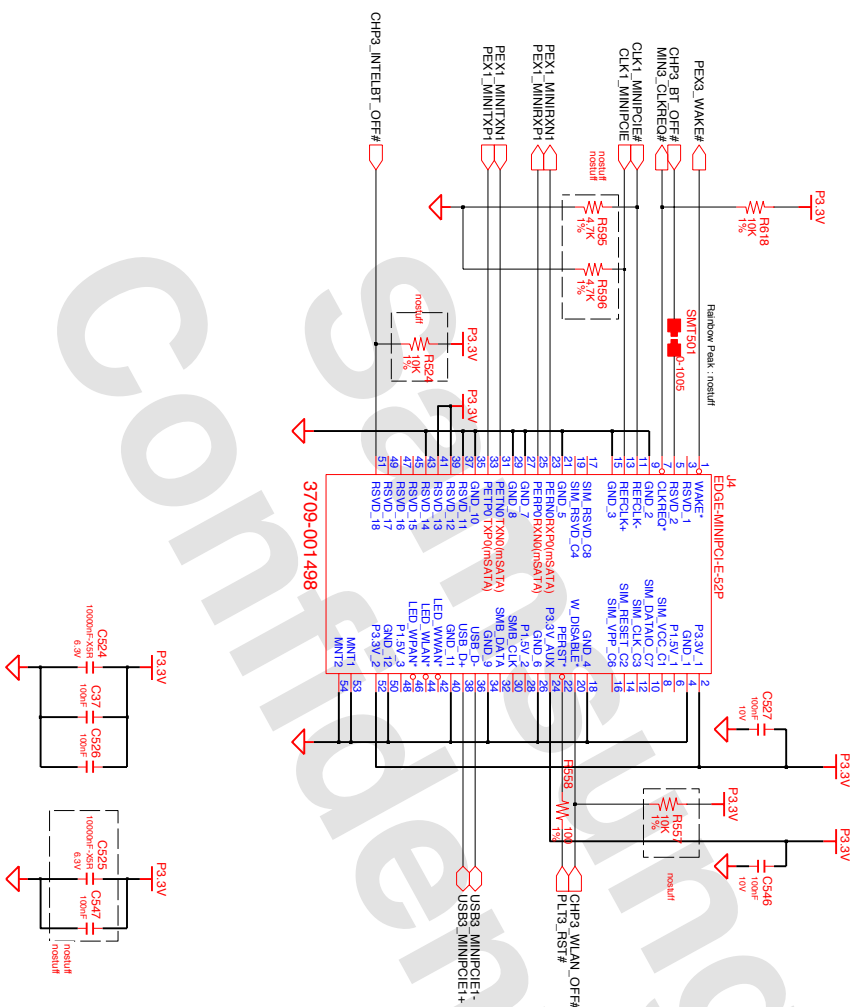


REVISION	DATE	FILE	SAMSUNG ELECTRONICS HDA CODEC & LED SUB AUDIO JACK & LED SUB January 20, 2011 16:59:20 PM	SAMSUNG ELECTRONICS BA41-01XXXXA
CHUCK	1/20/2011	Seal2R		
SAWU	ADV	HDA CODEC & LED SUB		
BC LEE	REV. 0.1	AUDIO JACK & LED SUB		
MODULE CODE	LAST EDIT			
	PAGE	29	OF	48

Reatek RTL8111E



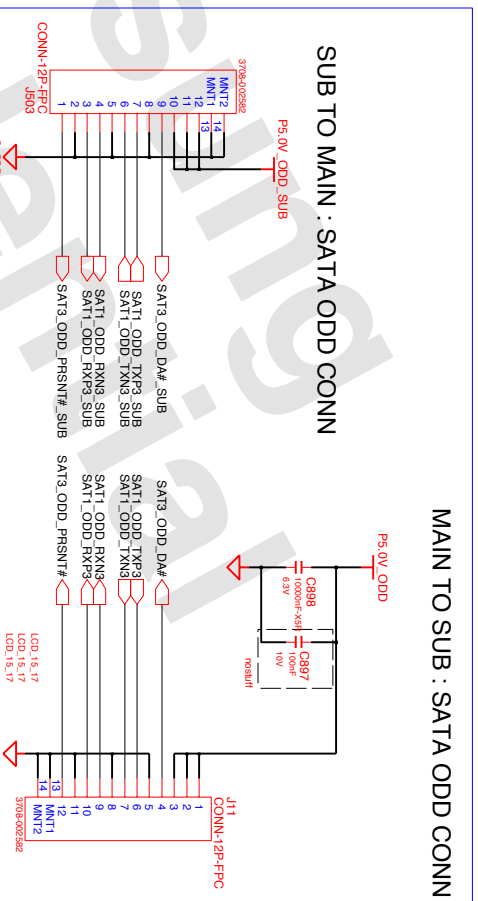
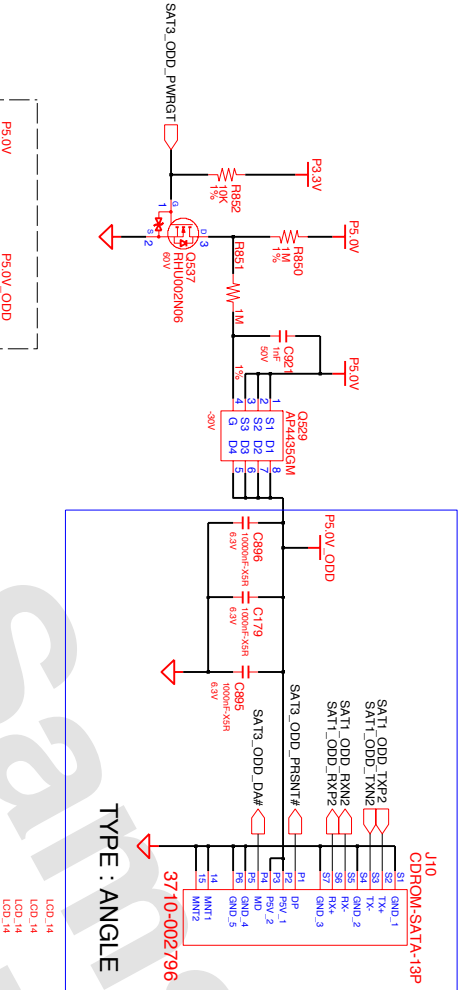
WLAN, 4mm



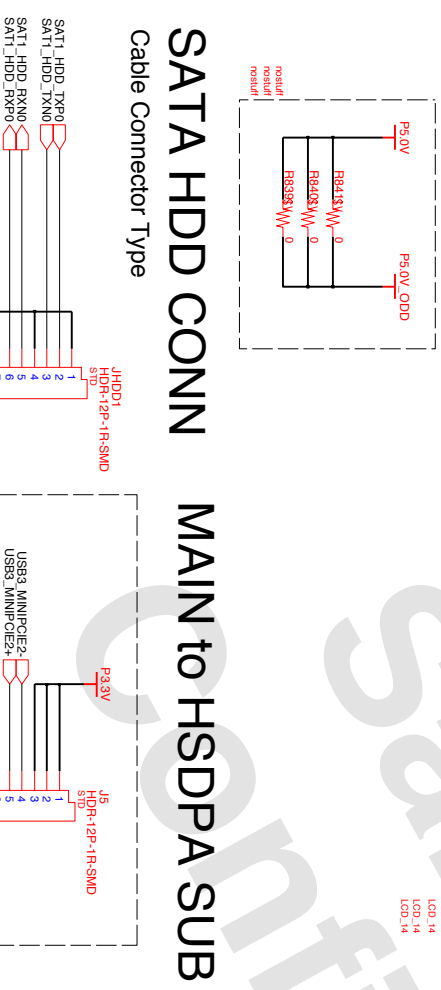
DESIGNER	GU HUI	DATE	1/02/2011	TITLE	Scala2-R	SAMSUNG
CHECK	SJ WU	REV. STEP	ADV		MINI_PCIE_CONN	ELECTRONICS
APPROVAL	BC LEE	REV	REV. 0.1		WLAN	BA41-01XXXX
MODULE CODE		LAY. EDIT		January 20, 2011 18:59:20 PM	PAGE	31 OF 48

SATA I/F CONN

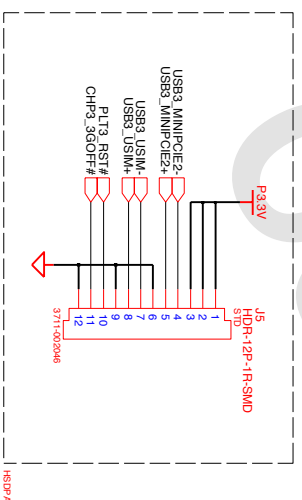
SATA ODD CONN



SATA ODD CONN

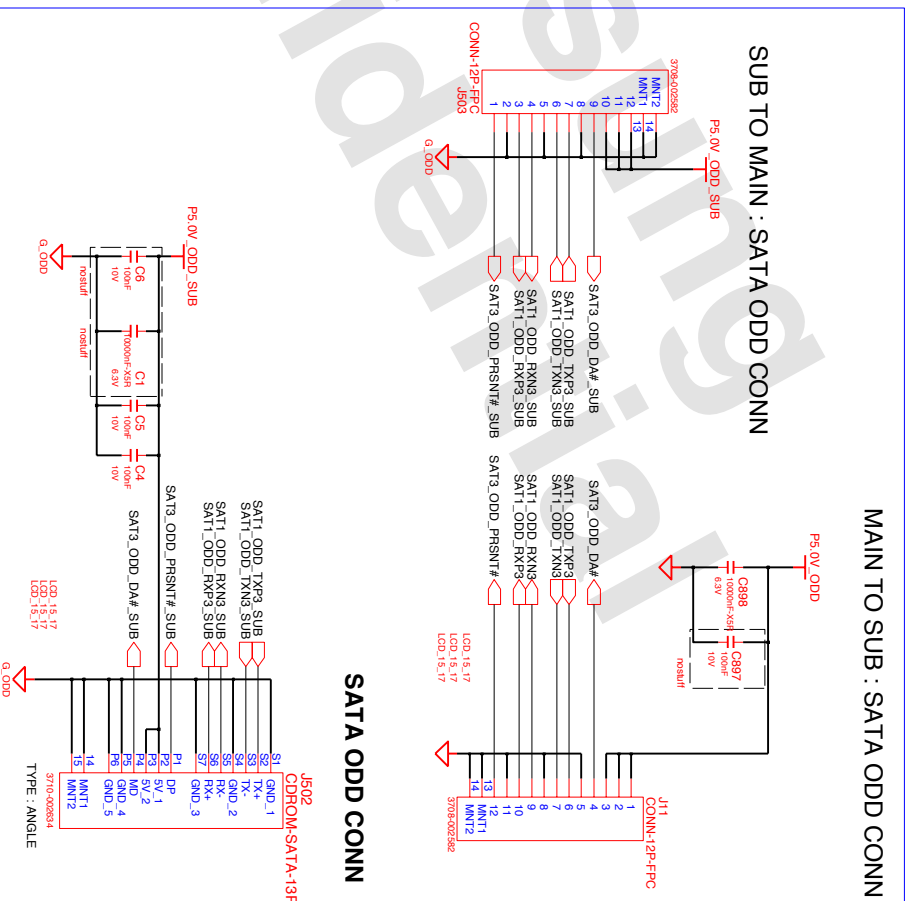


MAIN to HSDPA SUB

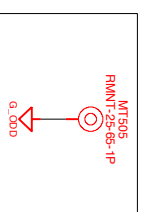


15.6" / 17.3"

MAIN TO SUB : SATA ODD CONN

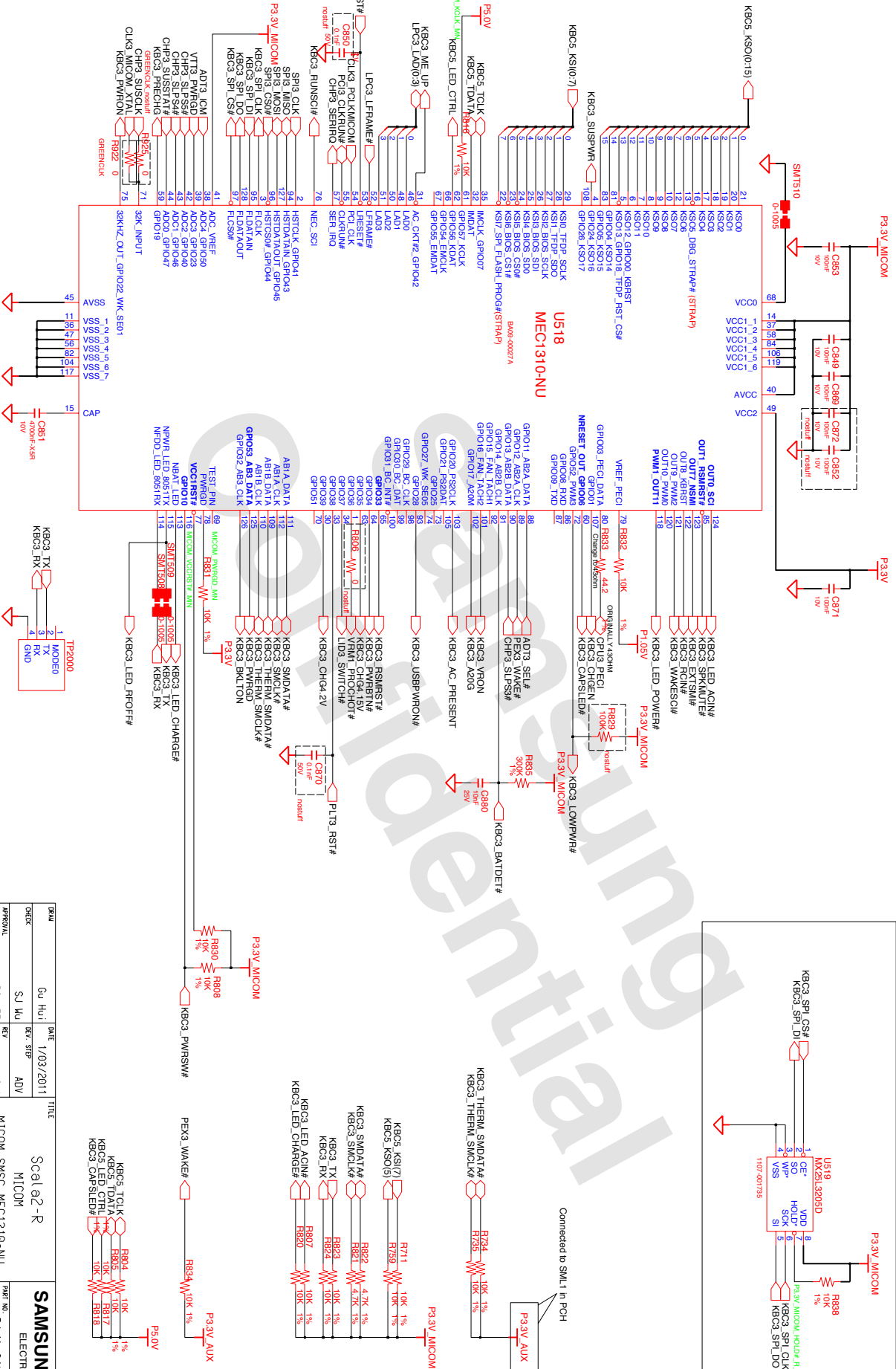


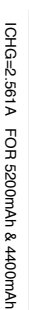
ODD sub board mount hole



DESIGNER	GU HUI	DATE	1/02/2011	FILE	Scal2R	SAMSUNG
CHECK	SJ WU	REV. STEP	ADV		SATA DEVICES	ELECTRONICS
APPROVAL	BC LEE	REV.	REV. 0.1		SATA I/F Conn	BA41-01XXXX
MODULE CODE		LAST EDIT	January 20, 2011 16:59:20 PM	PAGE	32	OF 48

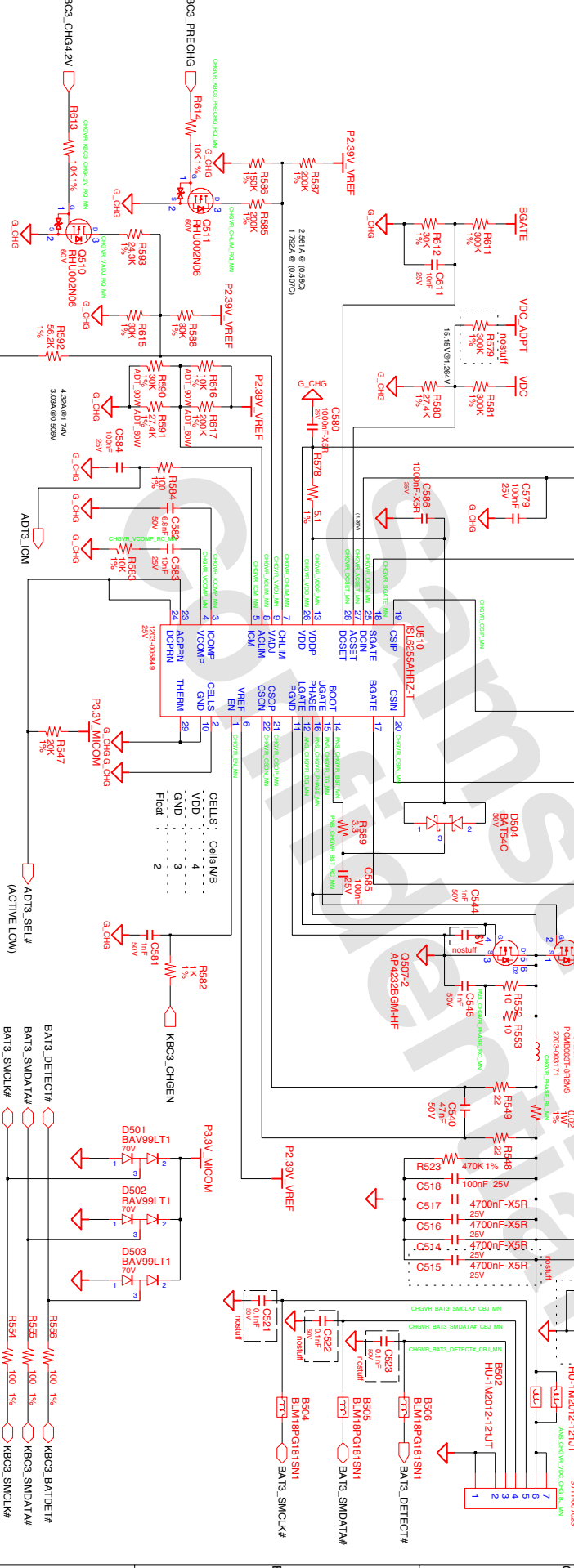
MAIN BOARD (MCOM)





KBC3_CHG4.2V	KBC3_CHG4.15V	VCHG
0	0	12.597V
1	0	12.471V
0	1	12.366V
1	1	12.311V

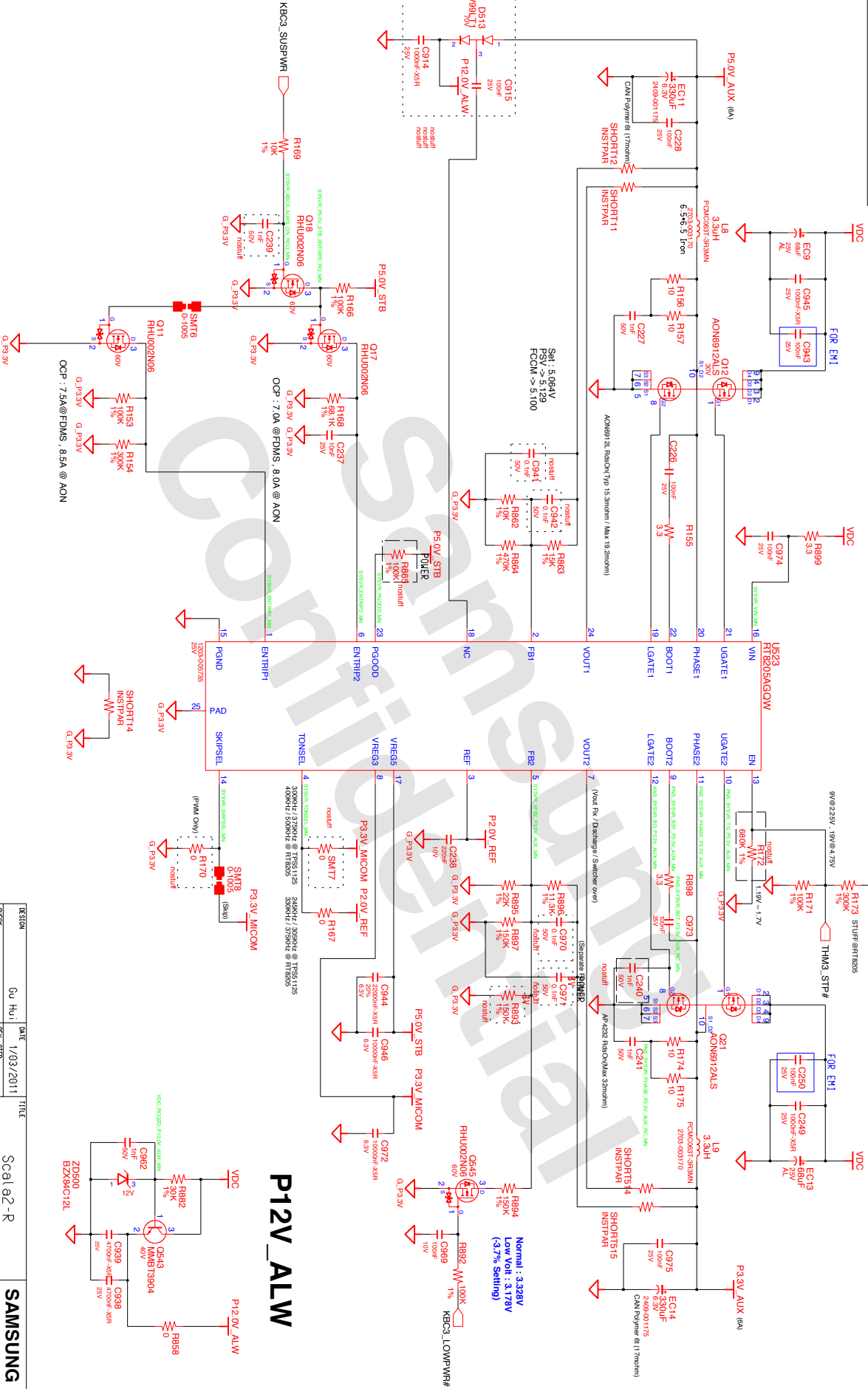
ICHG=2.561 A FOR 5200mAh & 4400mAh



Adapter configuration			
60W / 3.03A / 96%		90W / 4.32A / 91.2%	
Value	SEC Code	Value	SEC Code
R1op	2007-007394	10K	2007-007142
R1ot	27 4K	2007-007274	30K
			2007-008275

DATE	DATE	TITLE	SAMSUNG ELECTRONICS BA41-01XXXX
QUANTITY	16/02/2011	SCAL2-R	
CHECK	OK / TRIP	ADV	
APPROVAL	SA WU	POWER	
MOBILE CODE	BC LEE	Power - Changer	
LAB TEST	FEW 0.1		
unavailable	January 20, 2011 15:50:25 DM	PAGE 36	OF 48

P3.3V_AUX & P5.0V_AUX

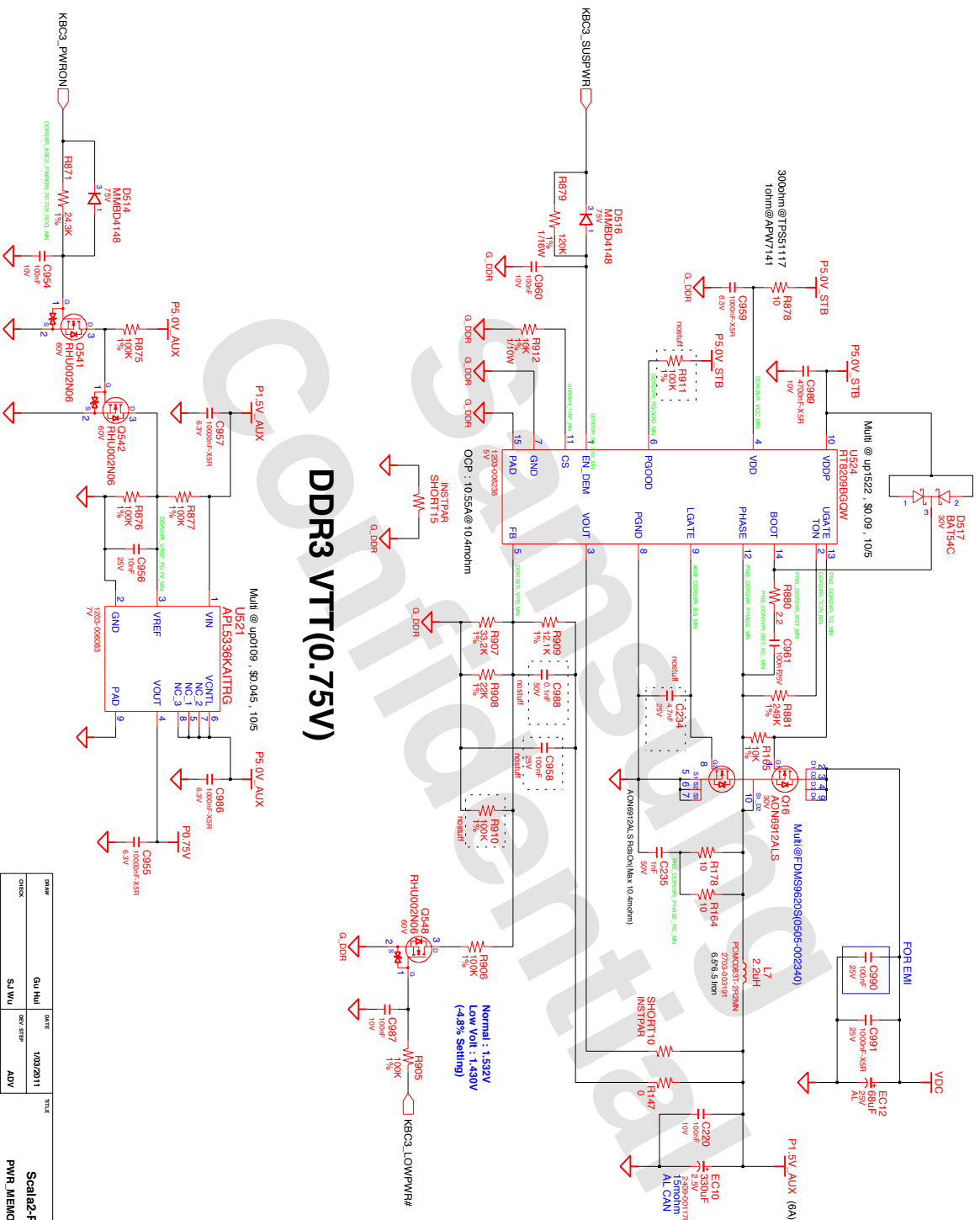


P12V_ALW

DESIGN	DATE	TITLE
SC81G2-R	1/03/2011	
DESIGN	BY: HUI	
CHECK	BY: STP	
APPROVAL	BY: ADV	
BC LEE	REV. 0.1	
MODULE CODE	JANUARY 20, 2011 18:59:20 PM	PAGE 37 OF 48

SAMSUNG
ELECTRONICS
THERM NO. BA-1-01XXXX

DDR3 Power



DDR3 VTT(0.75V)

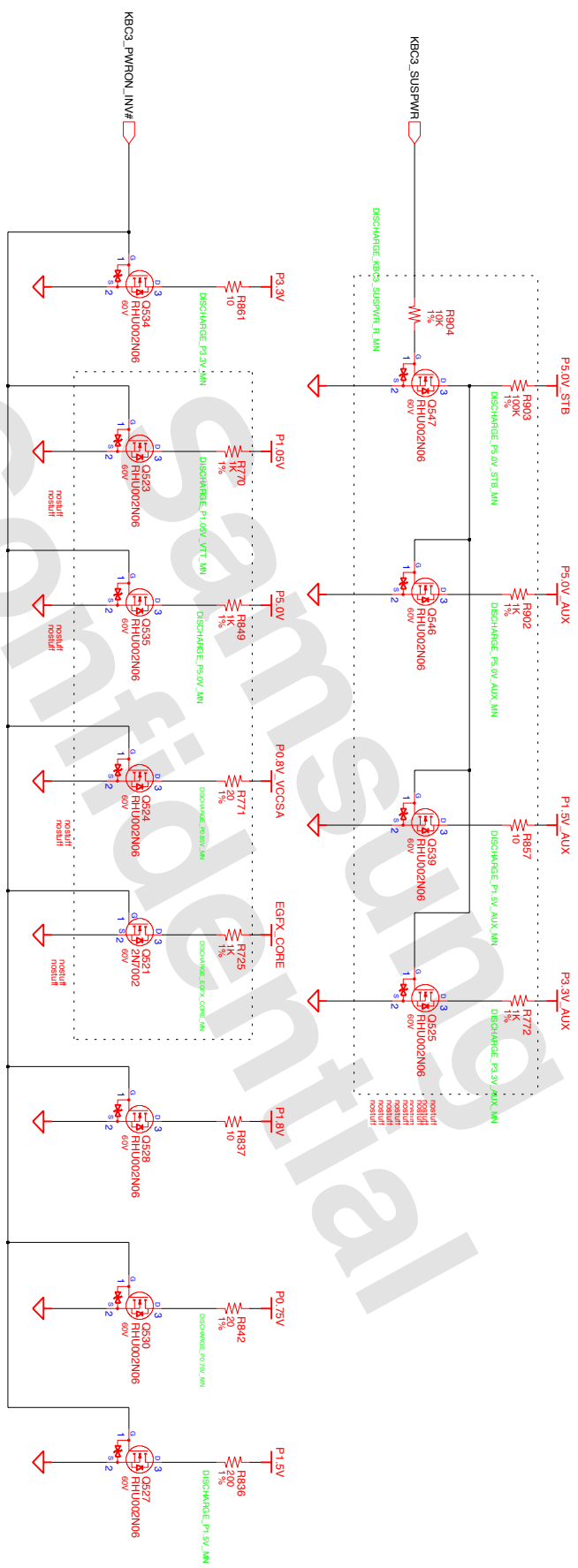
DATE	1/10/2011	FILE	Scale2R
CHECK	SJWU	REV. STEP	PWR MEMORY
APPROVAL	BC LEE	REV.	ADV
MODULE CODE	undefined	LAST EDIT	January 20, 2011 18:58:20 PM
		PAGE	40
		OF	48

SAMSUNG
ELECTRONICS
BA41-01XXXA

GFX Loadline -3.9mohm
CPU Loadline -1.9mohm

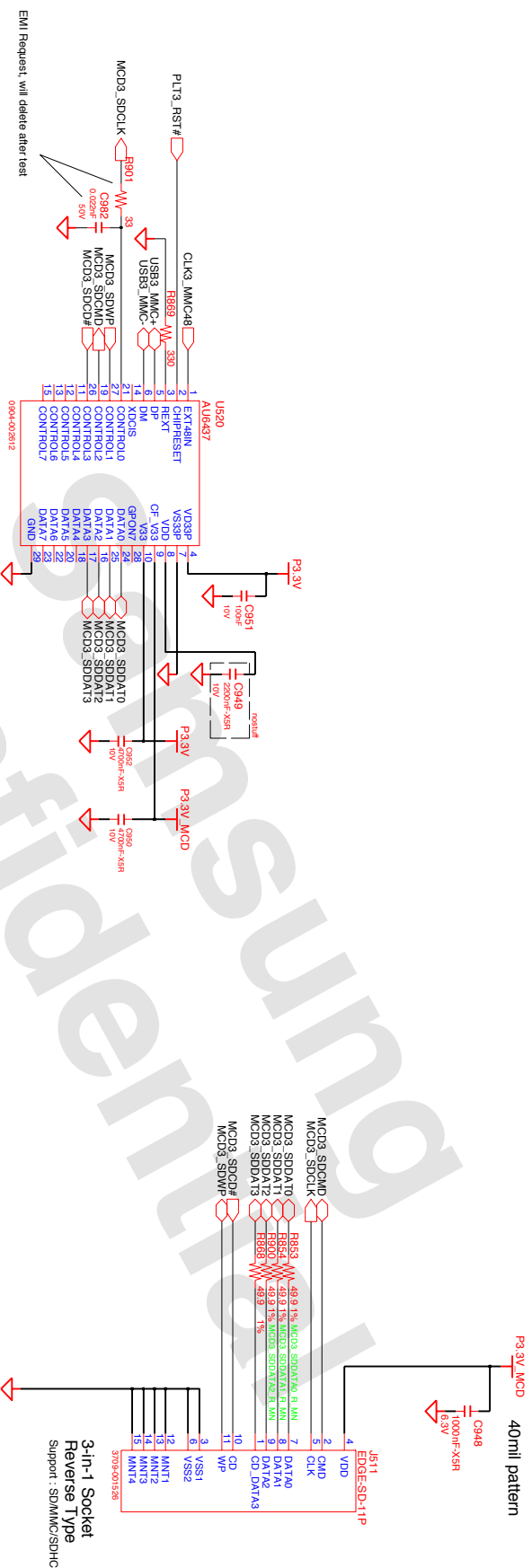


POWER DISCHARGER



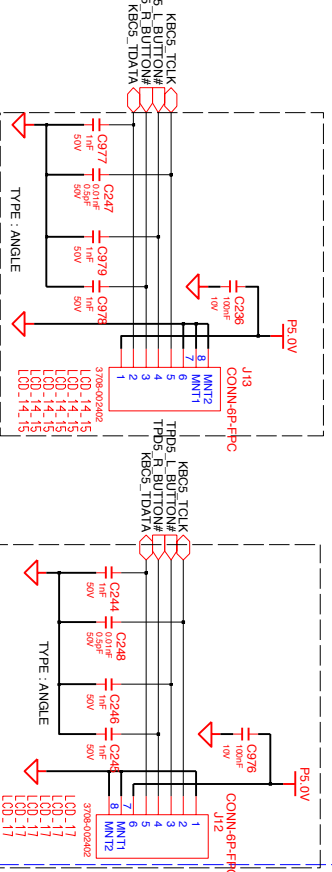
DESIGNER	GU HUI	DATE	1/03/2011	TITLE	Scala2R SUB BOARD1 Power Discharger	SAMSUNG ELECTRONICS
CHECK	SJ WU	REV. STEP	ADV			PART NO.
APPROVAL	BC LEE	REV.	REV. 0.1			BA41-01XXXX
MODULE CODE		LAY. EDIT		January 20, 2011 10:58:20 PM	PAGE	45 OF 48

AU6437 (4-in-1)

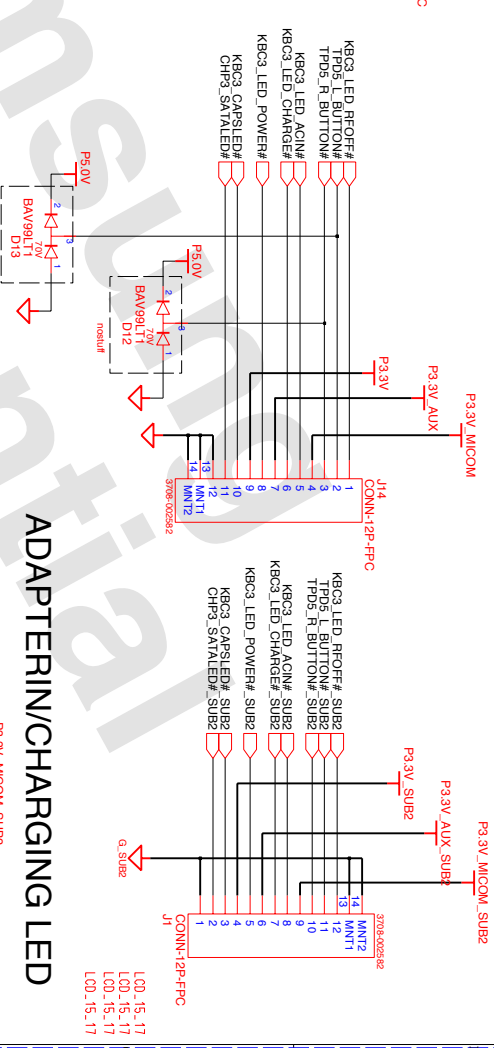


DESIGNER	MS YANG	DATE	1/10/2011	TITLE	Jimmo-R AU 6437	SAMSUNG ELECTRONICS
CHECKER	M.K.W	REV. STEP	ADV			
APPROVAL	BL LEE	REV.	REV. 0.3		AU 6437	BA41-01XXXX
MODULE CODE		LATE EDIT		January 18, 2011 08:10:20 AM	PAGE 46	OF 48

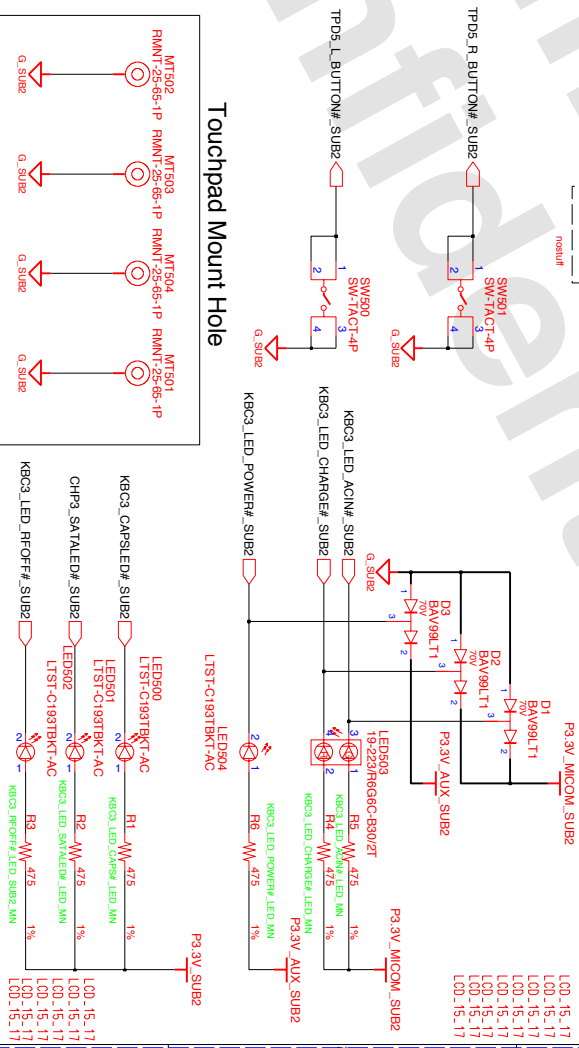
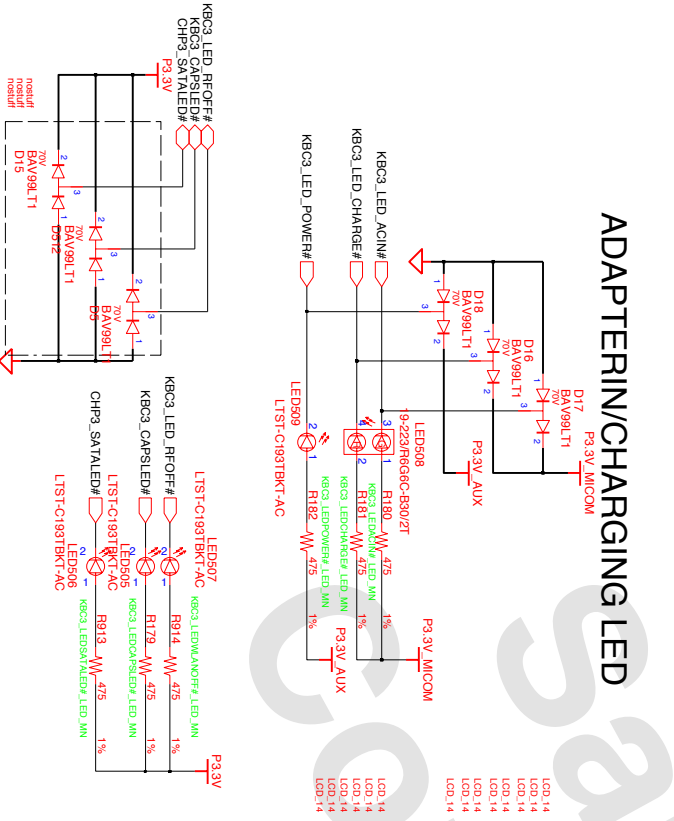
TOUCHPAD



15.6" & 17.3" TOUCHPAD MAIN TO SUB TOUCHPAD SUB TO MAIN



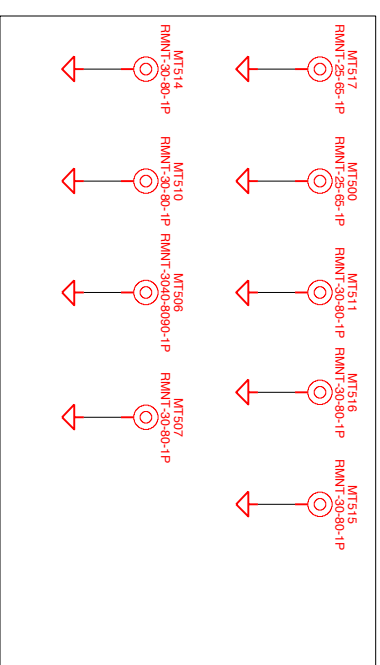
ADAPTERIN/CHARGING LED



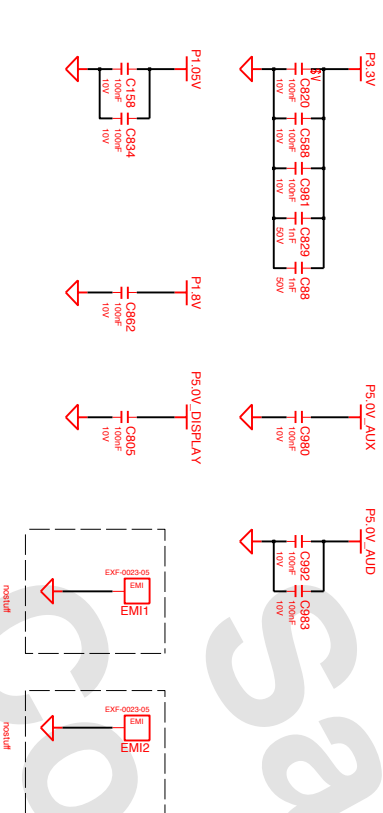
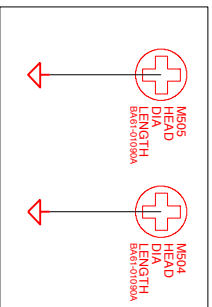
Touchpad Mount Hole

DATE	BY	DATE	FILE	REVISION	DESCRIPTION
2011/10/20	MS YANG	2011/10/20	1002/2011	REV. 0.1	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.2	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.3	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.4	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.5	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.6	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.7	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.8	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 0.9	Initial Design
2011/11/16	MS YANG	2011/11/16	1002/2011	REV. 1.0	Initial Design

Mainboard Mount ((25-65)X2EA, (30-80)X7EA)



PCH Thermal



DESIGNER	MS YANG	DATE	1/03/2011	TITLE	Jimbo-R	SAMSUNG
CHECK	MKKM	REV. STEP	ADV	MNT hole	ELECTRONICS	
APPROVAL	BL LEE	REV.	REV. 0.3	MNT hole		BA41-01XXXX
MODULE CODE		LAST EDIT	January 18, 2011 08:10:20 AM	PAGE	48	OF 48